

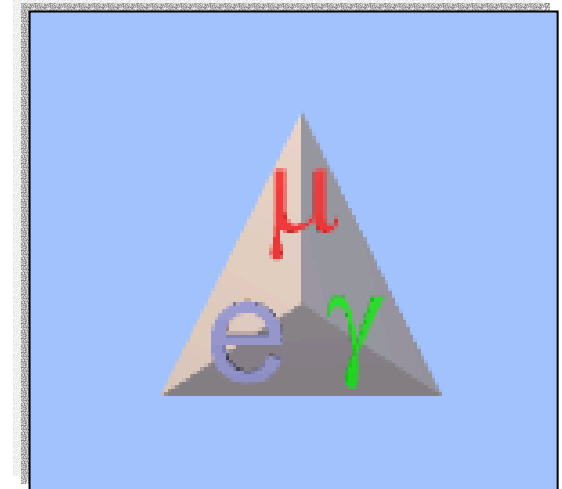
The MEG Experiment: Search for $\mu \rightarrow e \gamma$ at PSI

For the MEG collaboration

Stefan Ritt

(Paul Scherrer Institute, Switzerland)

- Motivation
- Experimental Technique
- New Electronics
- Current status

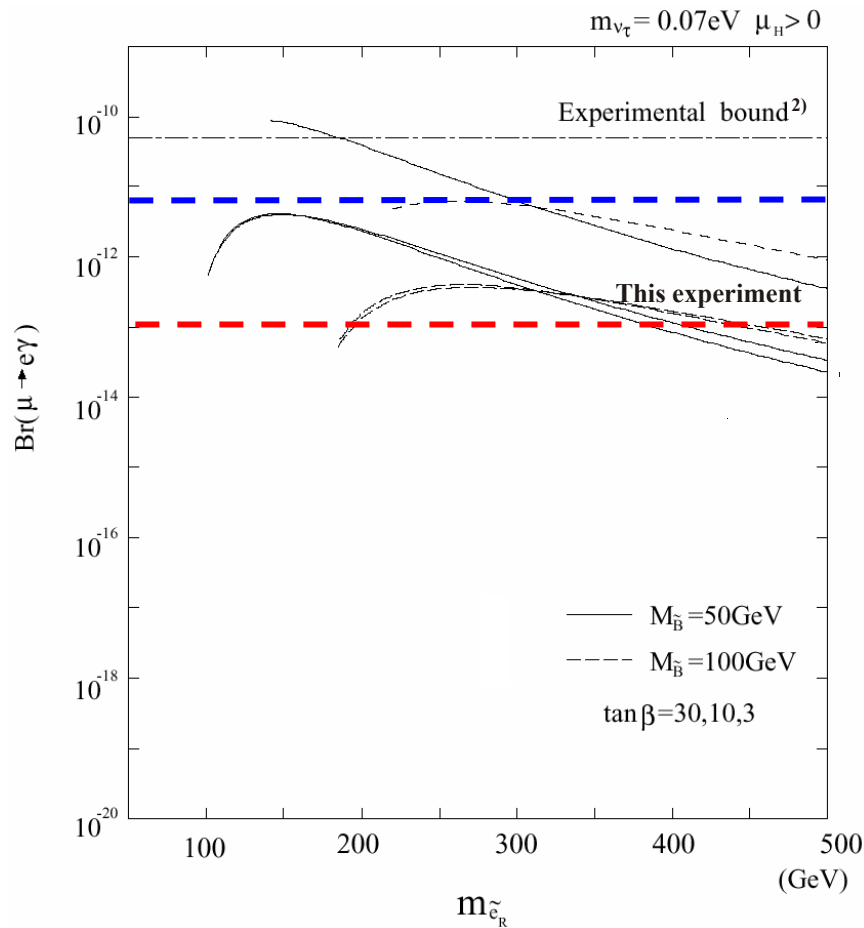


Motivation

Physics Motivation

- Minimal SM: Conservation of Baryons (proton decay) and Lepton Flavour (~~$\mu^+ \rightarrow e^+ \gamma$~~ , $\mu^+ \rightarrow e^+ \nu_e \nu_\mu \gamma$ ok)
- Super Symmetry (SUSY) theories generically predict LFV
- Processes like $\mu^+ \rightarrow e^+ \gamma$ are not "contaminated" by SM processes and therefore very clean
- Discovered ν oscillations are expected to enhance LFV rate
- The search for $\mu^+ \rightarrow e^+ \gamma$ is therefore a promising field to find physics beyond the SM

Prediction from SUSY SU(5)¹⁾

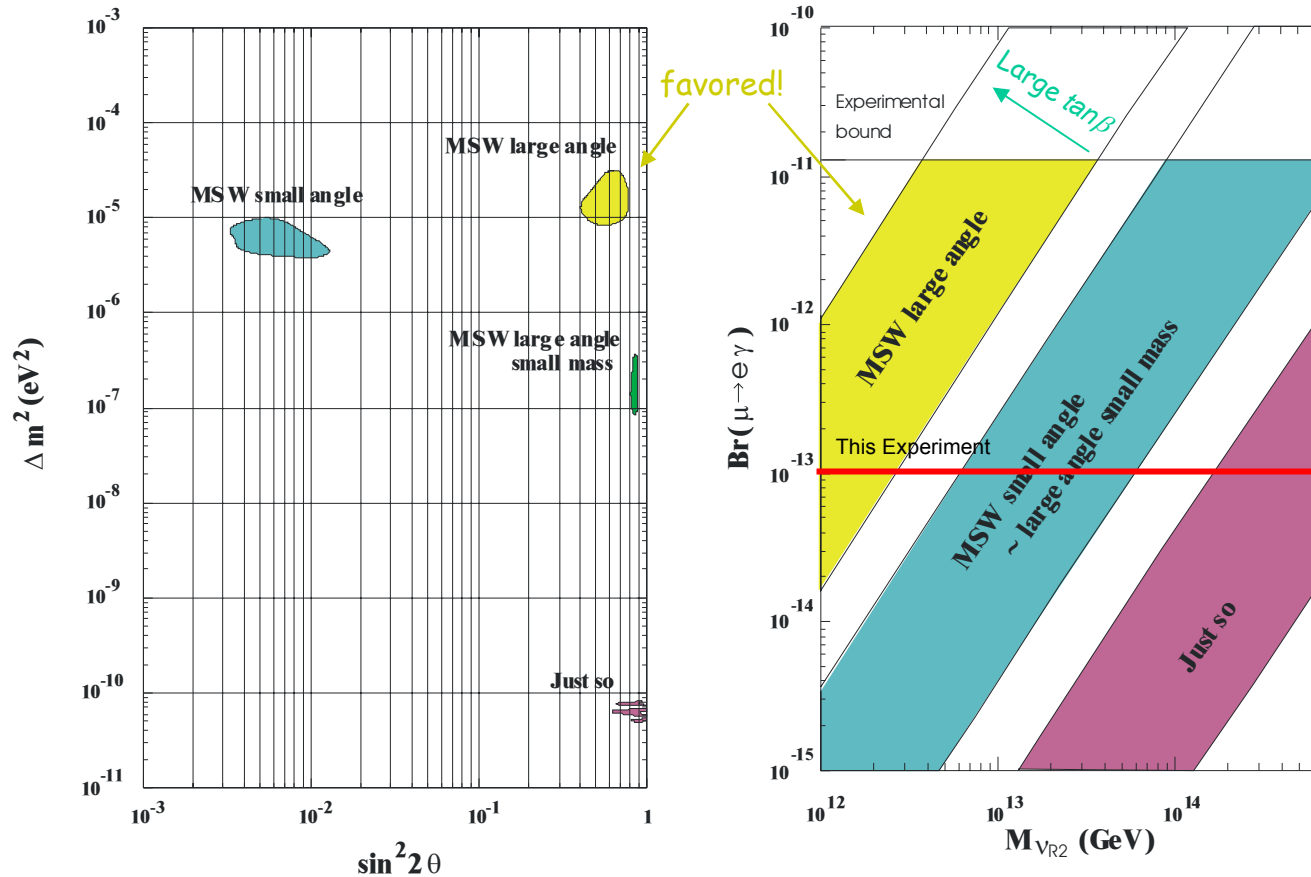


w/o SUSY: $BR(\mu^+ \rightarrow e^+ \gamma) \sim 10^{-24}$

- Based on observation of atmospheric neutrino anomaly
- $BR(\mu^+ \rightarrow e^+ \gamma) \geq 10^{-14}$
- $BR(\tau^\pm \rightarrow \mu^\pm \gamma) \geq 10^{-9}$
- $Conversion(\mu \rightarrow e) \geq 10^{-16}$

- 1) J. Hisano *et al.*, hep-ph/9711348
- 2) MEGA collaboration, hep-ex/9905013

Connection with ν oscillations¹⁾

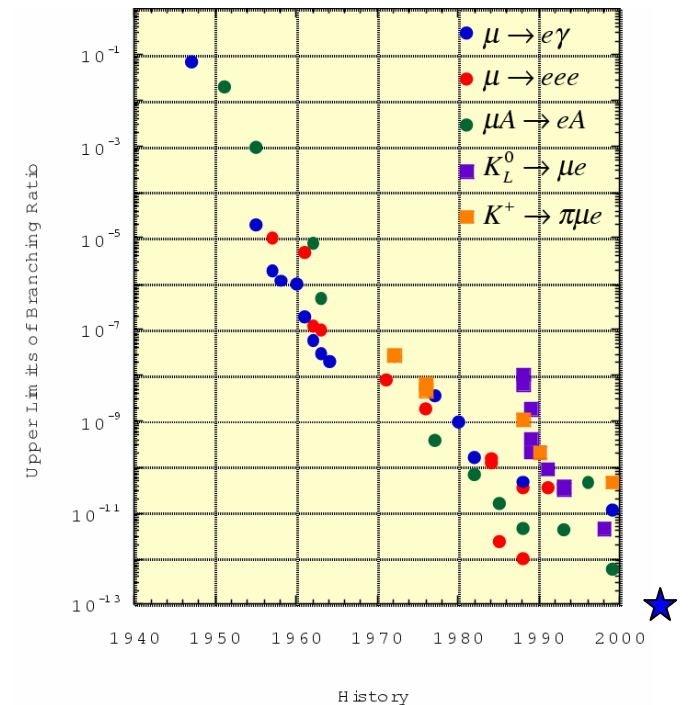


J. Hisano and D. Nomura, Phys. Rev. D59 (1999) 116005

Previous $\mu^+ \rightarrow e^+ \gamma$ Experiments

Place	Year	Upper limit	Author
SIN (PSI), Switzerland	1977	$< 1.0 \times 10^{-9}$	A. Van der Schaaf <i>et al.</i>
TRIUMF, Canada	1977	$< 3.6 \times 10^{-9}$	P. Depommier <i>et al.</i>
LANL, USA	1979	$< 1.7 \times 10^{-10}$	W.W. Kinnison <i>et al.</i>
LANL, USA	1986	$< 4.9 \times 10^{-11}$	R.D. Bolton <i>et al.</i>
LANL, USA	1999	$< 1.2 \times 10^{-11}$	MEGA Collab., M.L. Brooks <i>et al.</i>
PSI	~2005	$\sim 10^{-13}$	<i>This Experiment</i>

Search for Lepton-Flavour Violation:



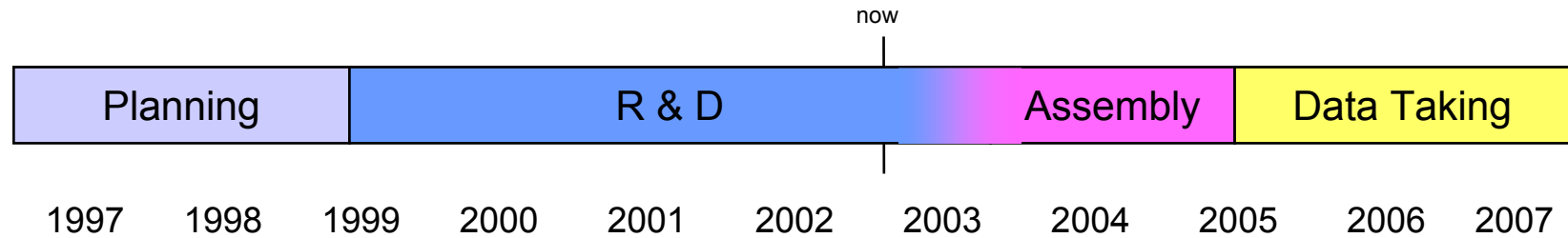
Two orders of magnitude improvement: **Challenge!**

MEG Collaboration

Institute	Country	Main Resp.	Head	Scientists	Students
ICEPP, Univ. of Tokyo	Japan	LXe Calorimeter	T. Mori	12	2
Waseda University	Japan	Cryogenics	T. Doke	5	2
INFN, Pisa	Italy	Trigger, M.C.	A. Baldini	5	4
INFN, Pavia	Italy	e ⁺ counter	G. Cecchet	3	-
IPNS, KEK, Tsukuba	Japan	Superconducting Solenoid	A. Maki	5	-
PSI	Switzerland	Drift Chamber, Beamline, DAQ	S. Ritt	4	-
BINP, Novosibirsk	Russia	LXe Tests and Purification	B. Khazin	5	-
Nagoya University	Japan	Cryogenics	K. Masuda	1	-

Σ 40 8

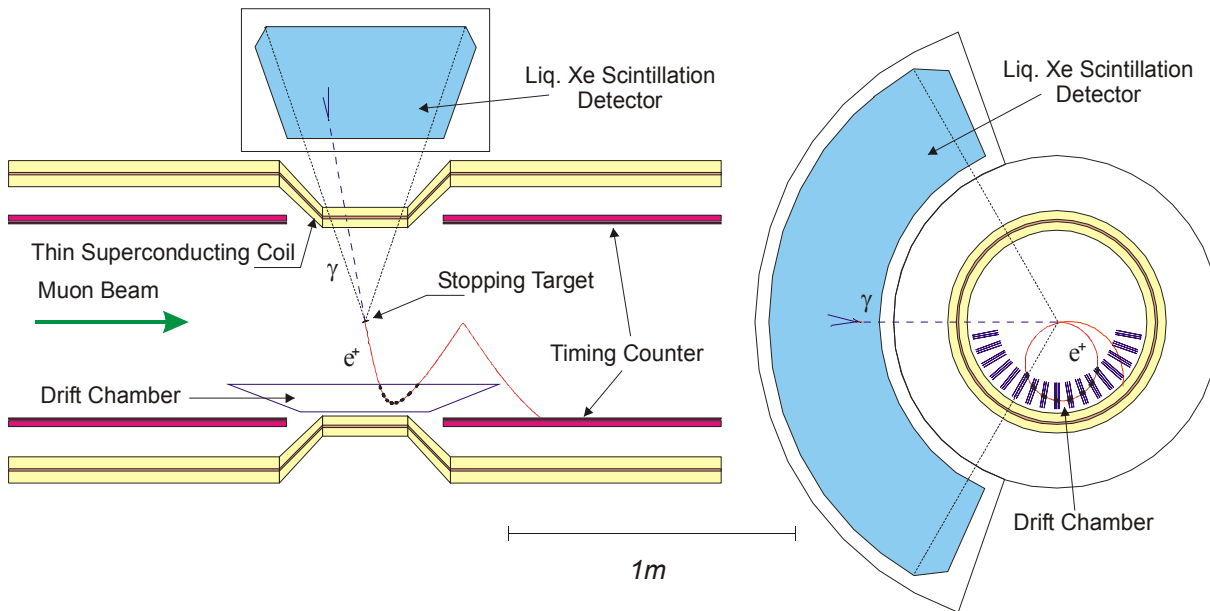
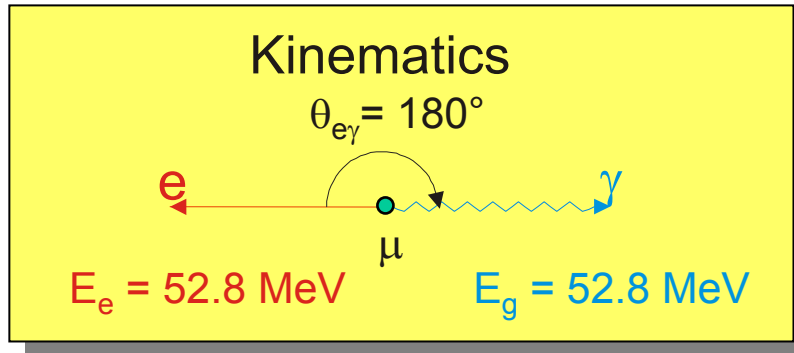
Time Table



"large prototype"

Experimental Method

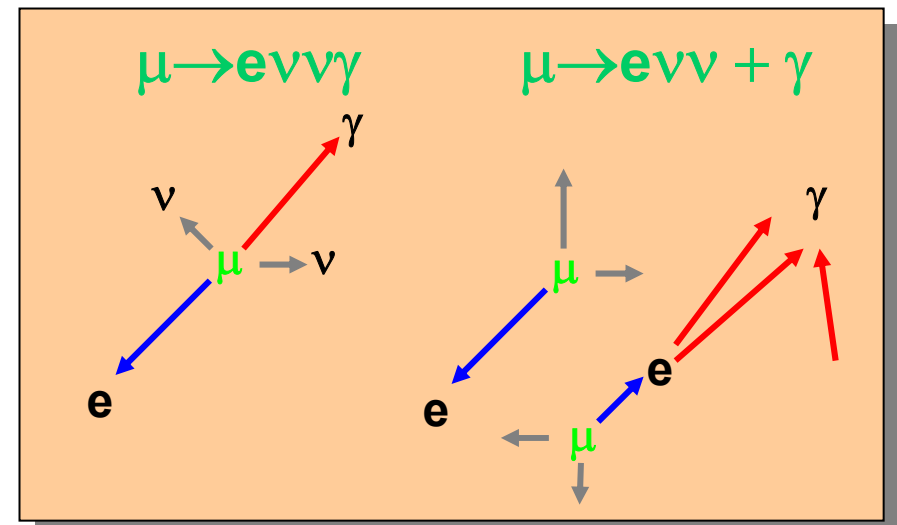
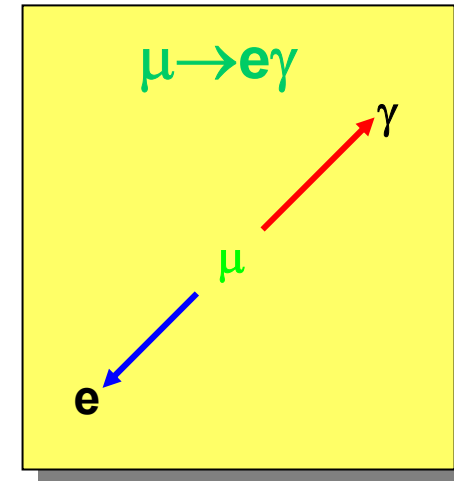
Detector Overview



- Stopped μ beam of 10^7 - 10^8 s^{-1} , 100% duty factor
- Liquid Xe calorimeter for γ detection
- Solenoidal magnetic spectrometer with gradient field
- Radial drift chambers for e^+ momentum determination
- Timing counter for e^+

Signal and Background

- $\mu^+ \rightarrow e^+ \gamma$ signal very clear
 - $E_\gamma = E_{e^+} = 52.8 \text{ MeV}$
 - $\theta_{\gamma e^+} = 180^\circ$
 - e^+ and γ in time
- Background
 - Radiative μ^+ decays
 - Accidental overlap
- Detector Requirements
 - Excellent energy resolution
 - Excellent timing resolution
 - Good angular resolution



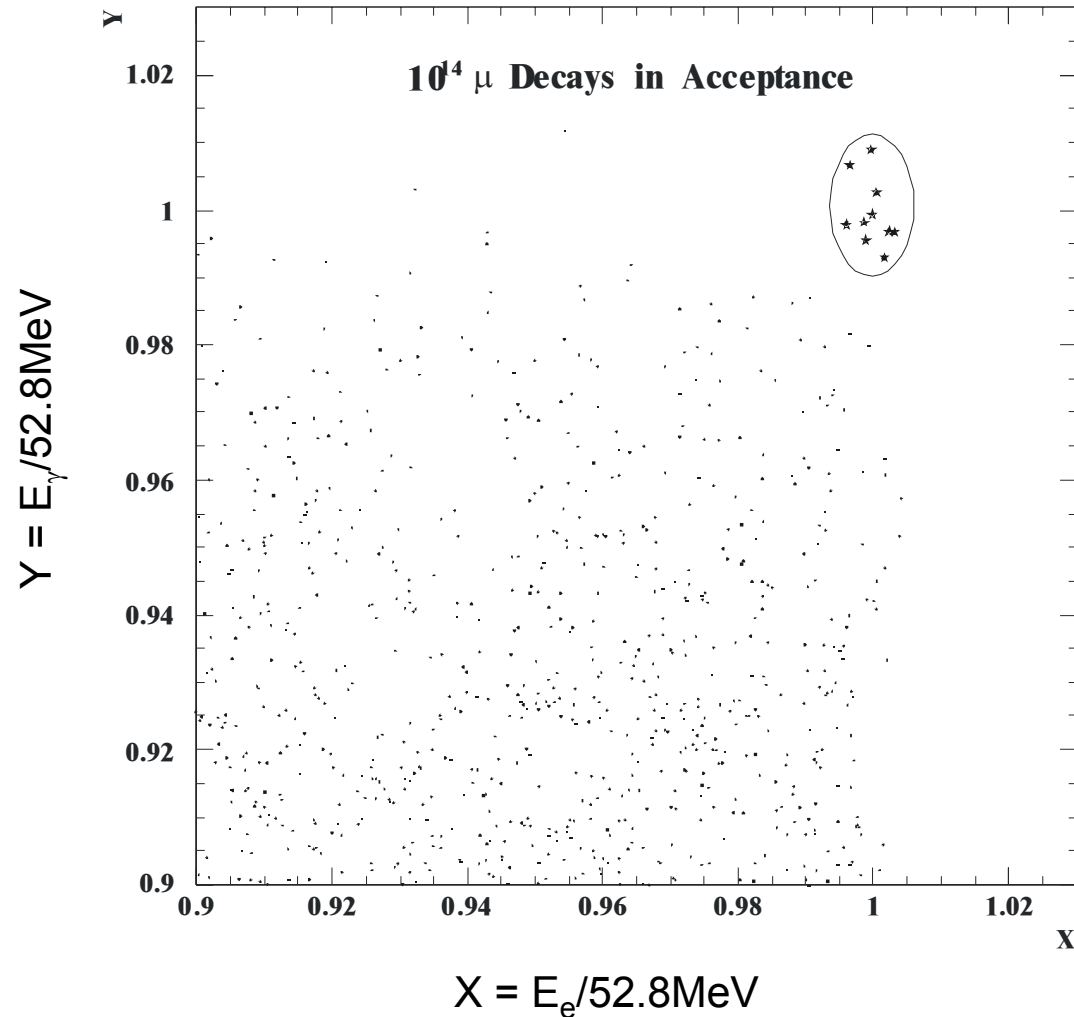
$\mu \rightarrow e \gamma$ Signature

$$\mu \rightarrow e \gamma$$

$$E_e, E_\gamma = 52.8 \text{ MeV}$$

$$\mu \rightarrow e \gamma \nu \nu$$

$$E_e, E_\gamma < 52.8 \text{ MeV}$$



Sensitivity and Background Rate

N_μ	2.5×10^7
T	2.6×10^7 s (~65 weeks)
$\Omega/4\pi$	0.09
ε_e	0.90
ε_γ	0.60
ε_{sel}	0.70

Aimed resolutions:

	FWHM
ΔE_e	0.8%
ΔE_γ	4.3%
$\Delta\theta_{e\gamma}$	19 mrad
$\Delta t_{e\gamma}$	140 ps

Single event sensitivity $(N_\mu \cdot T \cdot \Omega/4\pi \cdot \varepsilon_e \cdot \varepsilon_\gamma \cdot \varepsilon_{sel})^{-1} = 4.5 \times 10^{-14}$

Prompt Background $B_{pr} \cong 10^{-17}$

Accidental Background $B_{acc} \propto \Delta E_e \cdot \Delta t_{e\gamma} \cdot (\Delta E_\gamma)^2 \cdot (\Delta\theta_{e\gamma})^2 \rightarrow 3 \times 10^{-14}$

90% C.L. Sensitivity $\rightarrow 1.5 \times 10^{-13}$

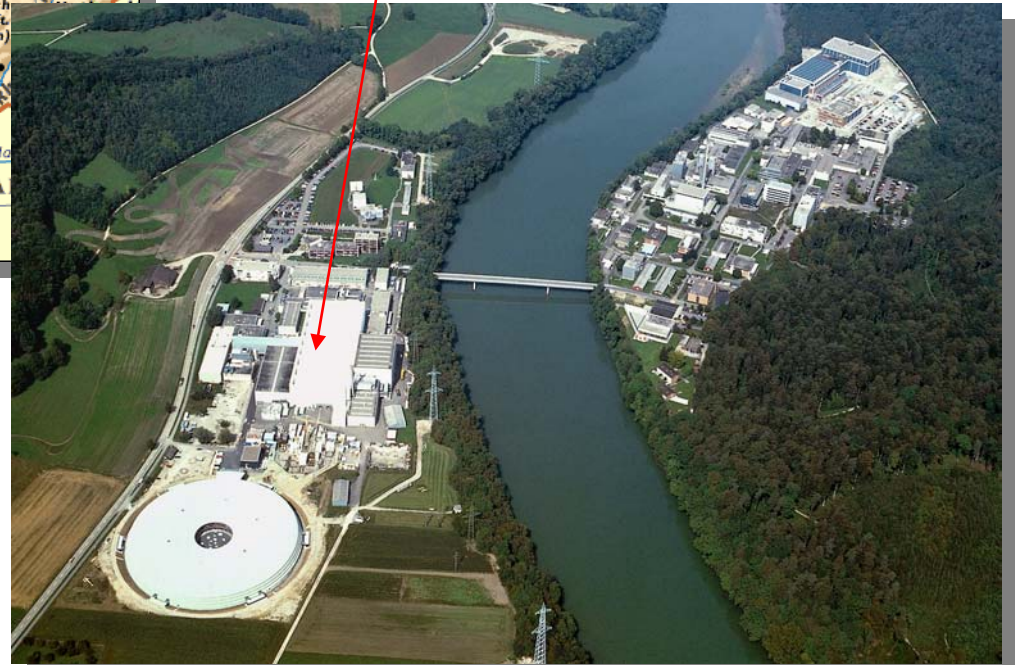
Comparison with previous experiments

Exp./Lab	$\Delta E_e/E_e$ %FWHM	$\Delta E_\gamma/E_\gamma$ %FWHM	$\Delta t_{e\gamma}$ (ns)	$\Delta\theta_{e\gamma}$ (mrad)	Inst. Stop rate (s^{-1})	Duty cycle (%)
SIN (PSI)	8.7	9.3	1.4	-	$(4..6) \times 10^5$	100
TRIUMF	10	8.7	6.7	-	2×10^5	100
LANL	8.8	8	1.9	37	2.4×10^5	6.4
Crystal Box	8	8	1.3	87	4×10^5	(6..9)
MEGA	1.2	4.5	1.6	17	2.5×10^8	(6..7)
MEG	0.8	4.3	0.14	19	2.5×10^7	100

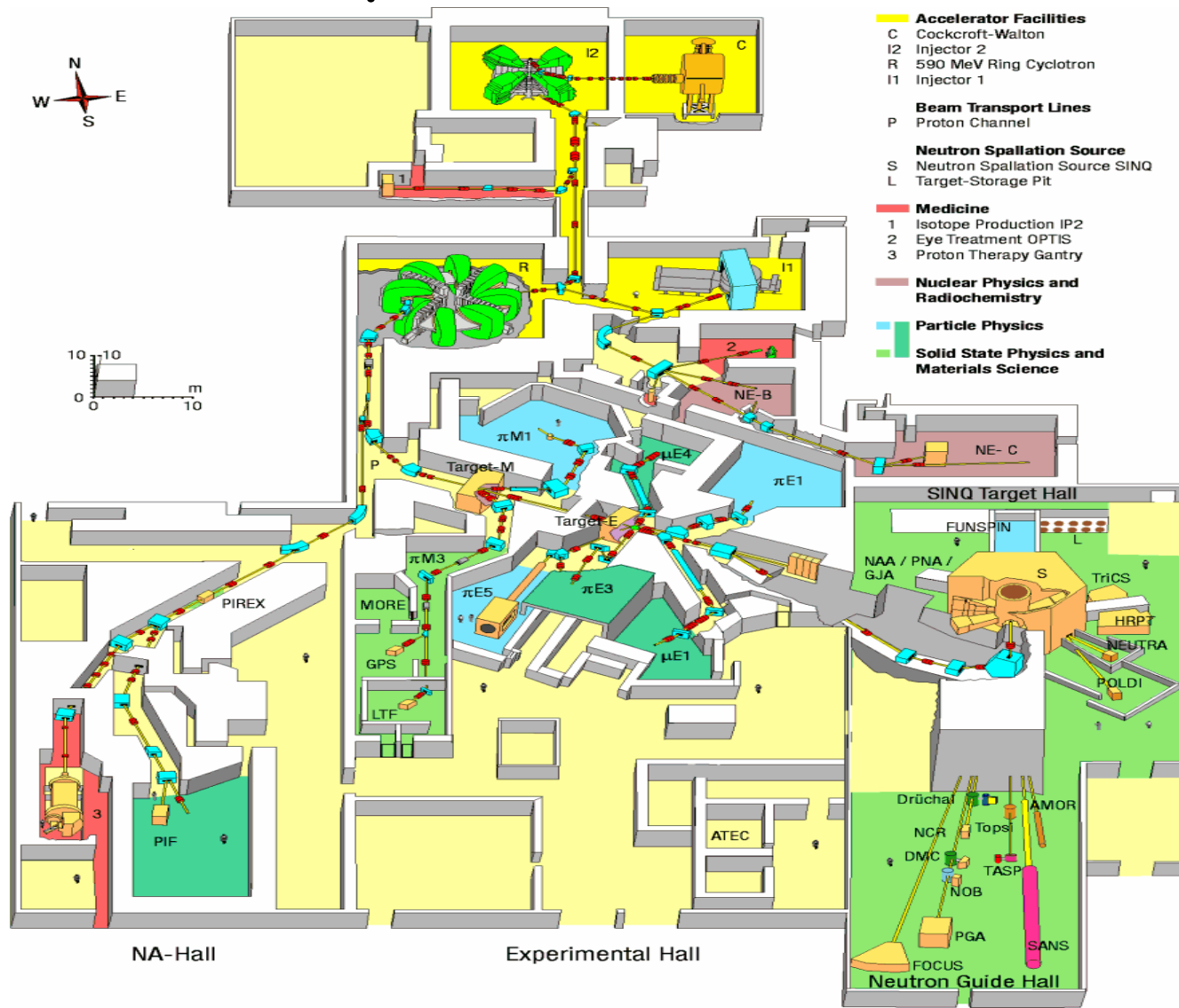
Paul Scherrer Institute



Experimental Hall

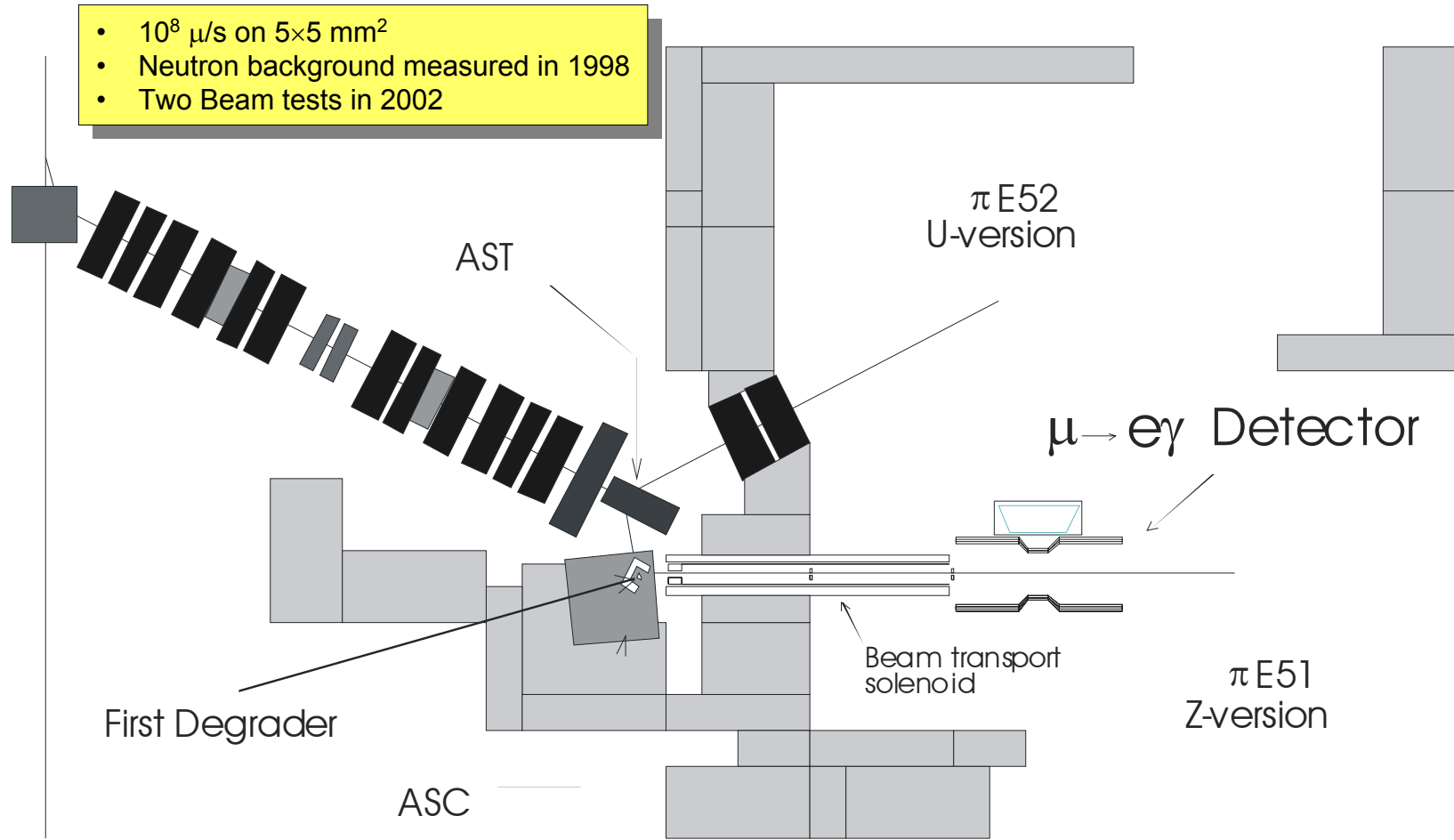


Experimental Hall

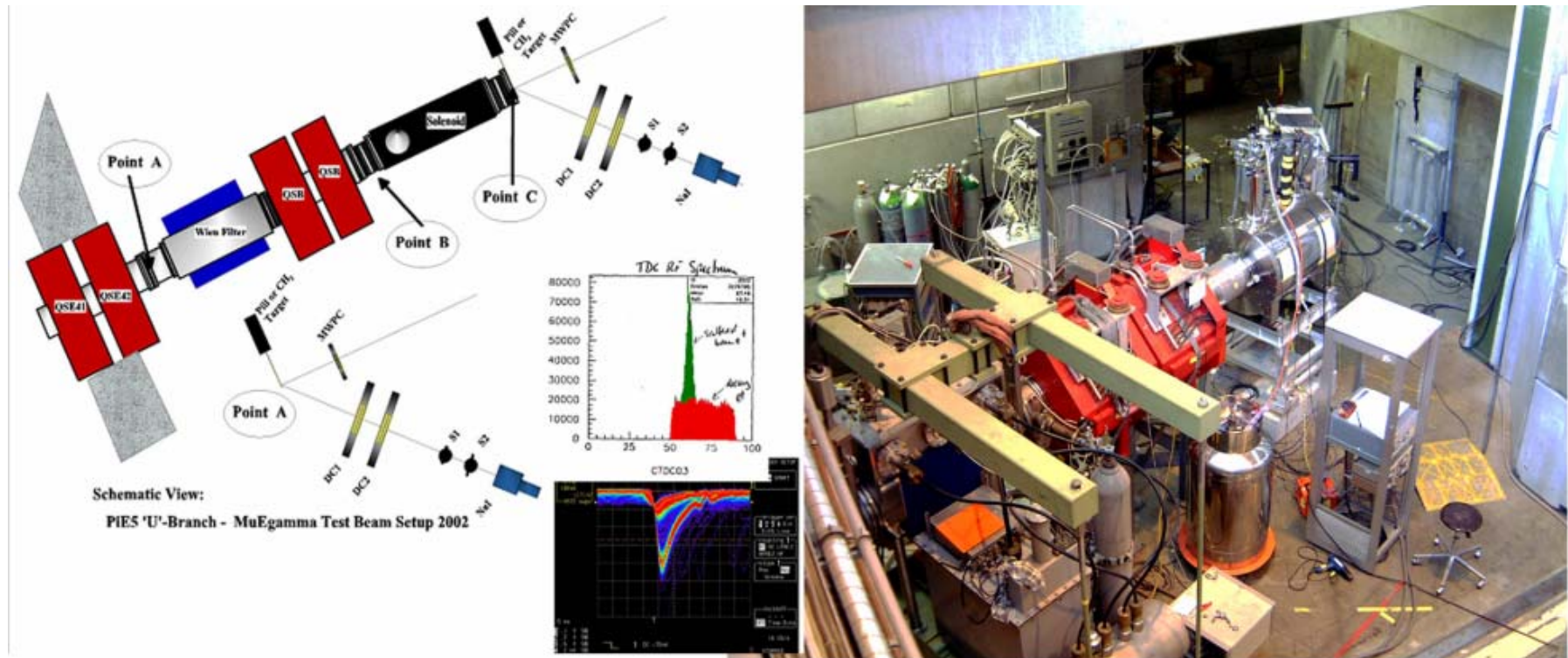


π E5 Beam Line

- $10^8 \mu/s$ on $5 \times 5 \text{ mm}^2$
- Neutron background measured in 1998
- Two Beam tests in 2002

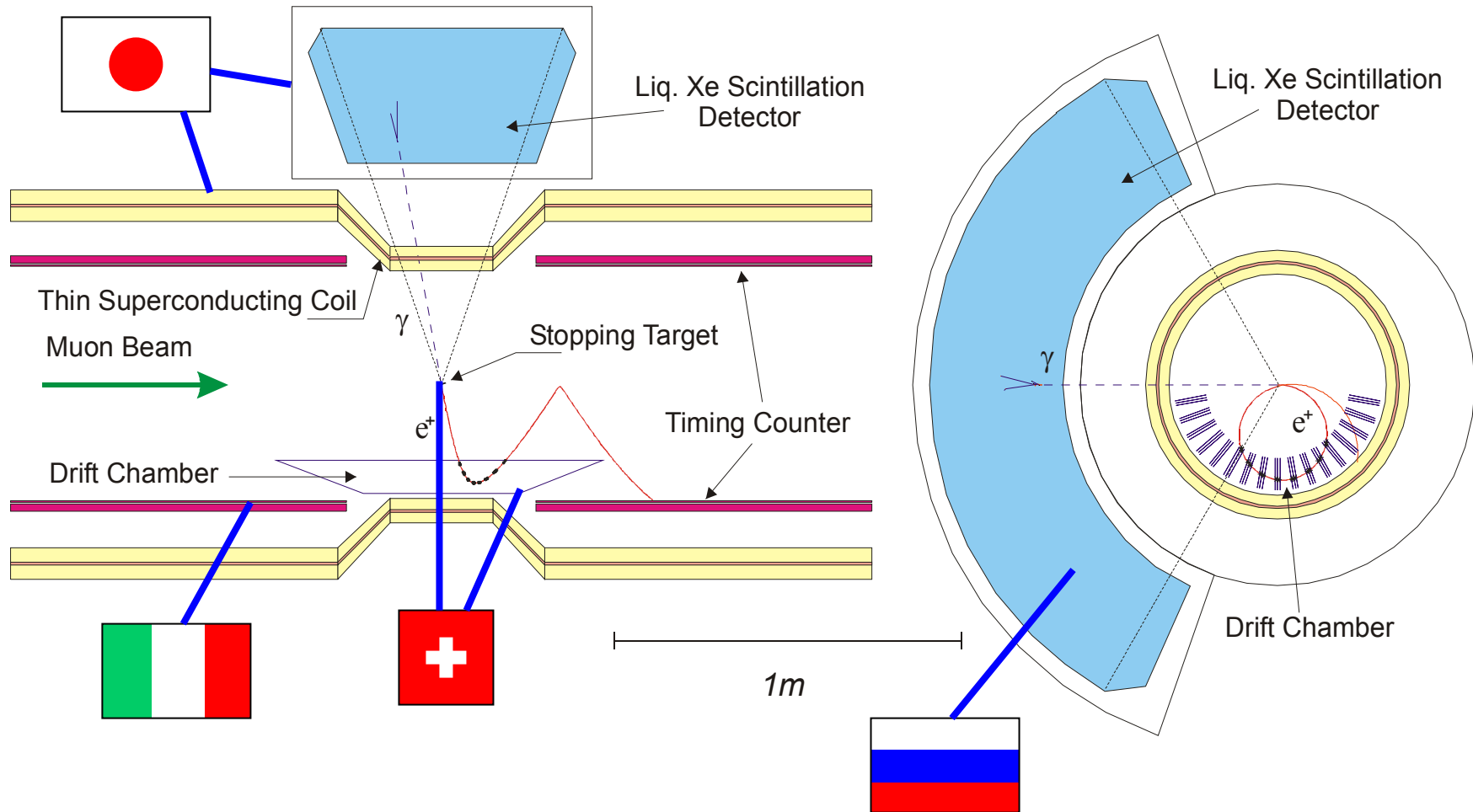


2002 beam line tests



- Electrostatic separator shows excellent background suppression ($7 - 11 \sigma$)
- Beam spot size at target $\sim 6\text{mm } \sigma$
- Stopping rate $\sim 0.5 - 0.8 \times 10^8$ (short production target)

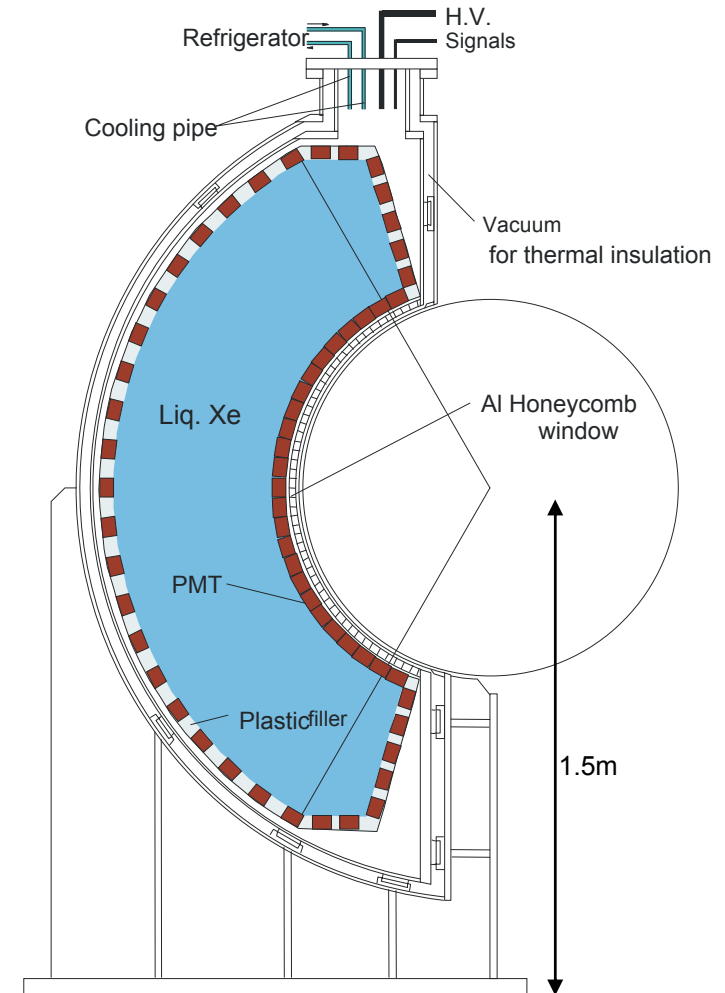
Detector



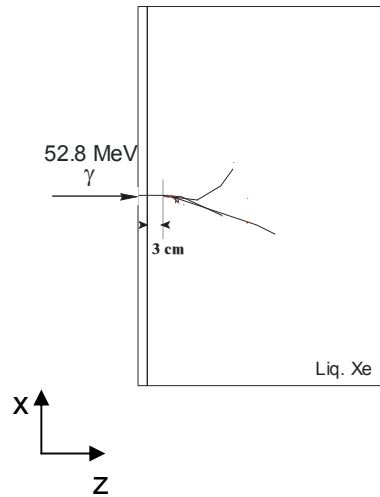
LXe Calorimeter

- ~800l liquid Xe (3t)
- ~800 PMTs immersed in LXe
- Only scintillation light detected
- Fast response (45 ns decay time)
- High light output (70% of NaI(Tl))¹⁾
- High uniformity compared with segmented calorimeters
- High channel occupancy will be accommodated by special trigger scheme

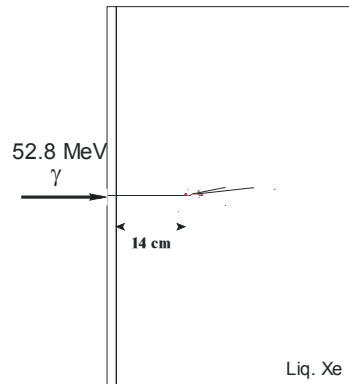
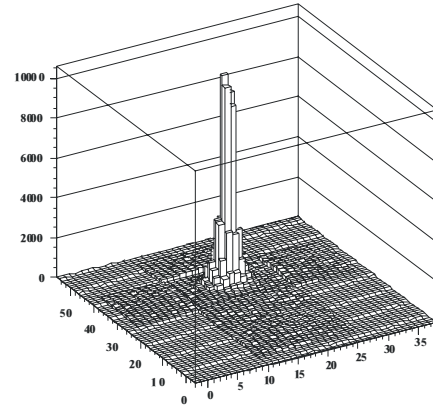
1) T. Doke and K. Masuda, NIM A 420 (1999) 62



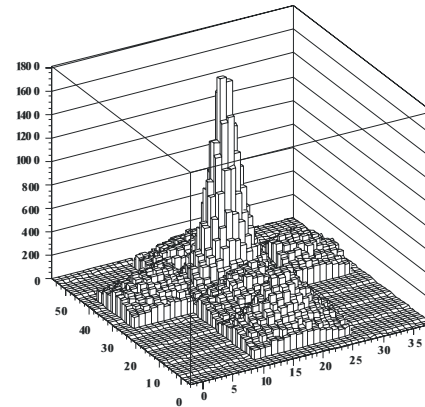
γ Response



(a)



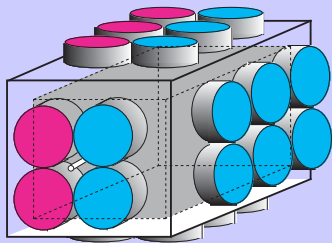
(b)



- Signal is distributed over many PMTs in most cases
- Weighted mean of PMTs on the front face
→ $\delta x \sim 10\text{mm FWHM}$
- Broadness of distribution
→ $\delta z \sim 16\text{mm FWHM}$
- Timing resolution
→ $\delta t \sim 100\text{ps FWHM}$
- Energy resolution
 $\sim 4.3\%$ FWHM
depends on light attenuation in LXe

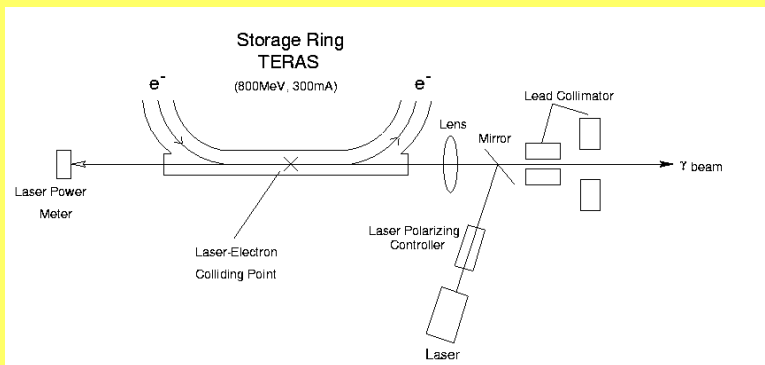
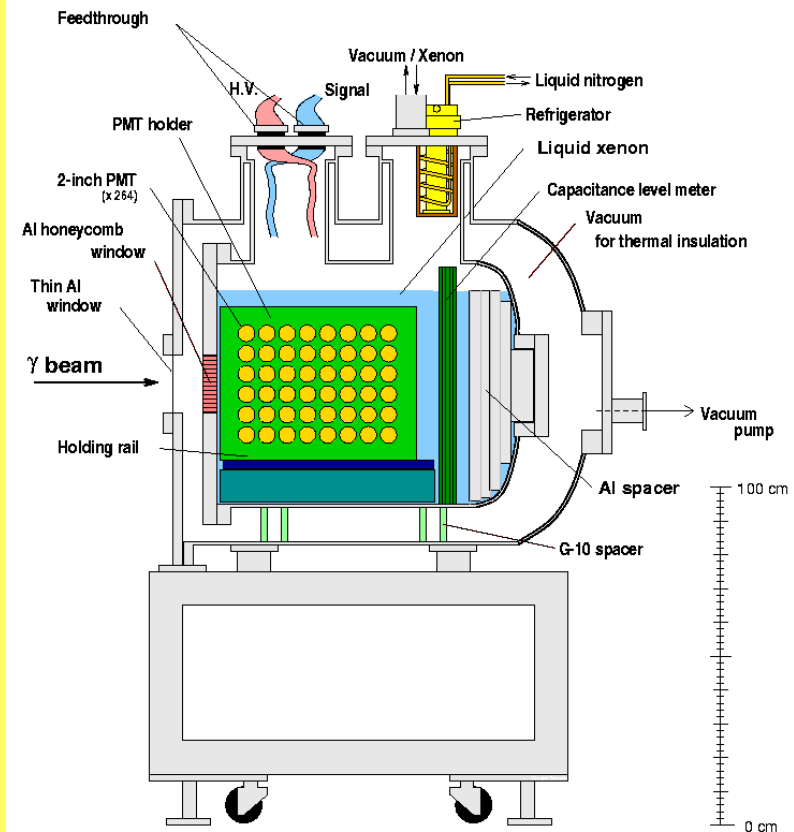
Calorimeter Prototypes

"Small"



- 32 PMTs, 2.3 l LXe
- Tested with radioactive sources ^{51}Cr , ^{137}Cs , ^{54}Mn , ^{88}Y
- Extrapolated resolutions at 52.8 MeV in agreement with quoted numbers

"Large"



Measure resolutions with 40 MeV photon beam at ETL, Tsukuba, Japan

Large Prototype

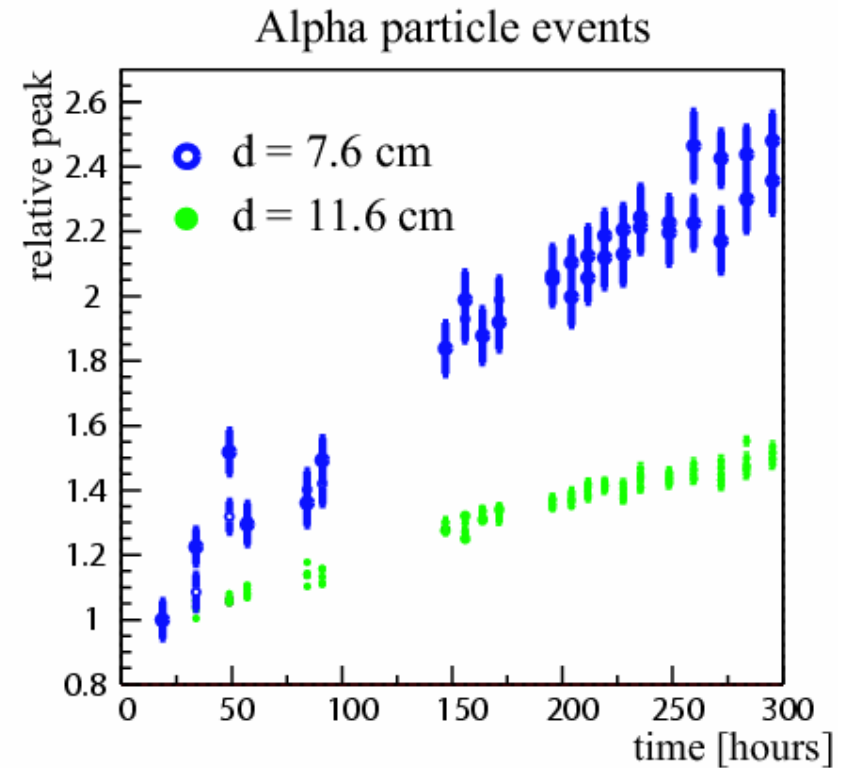
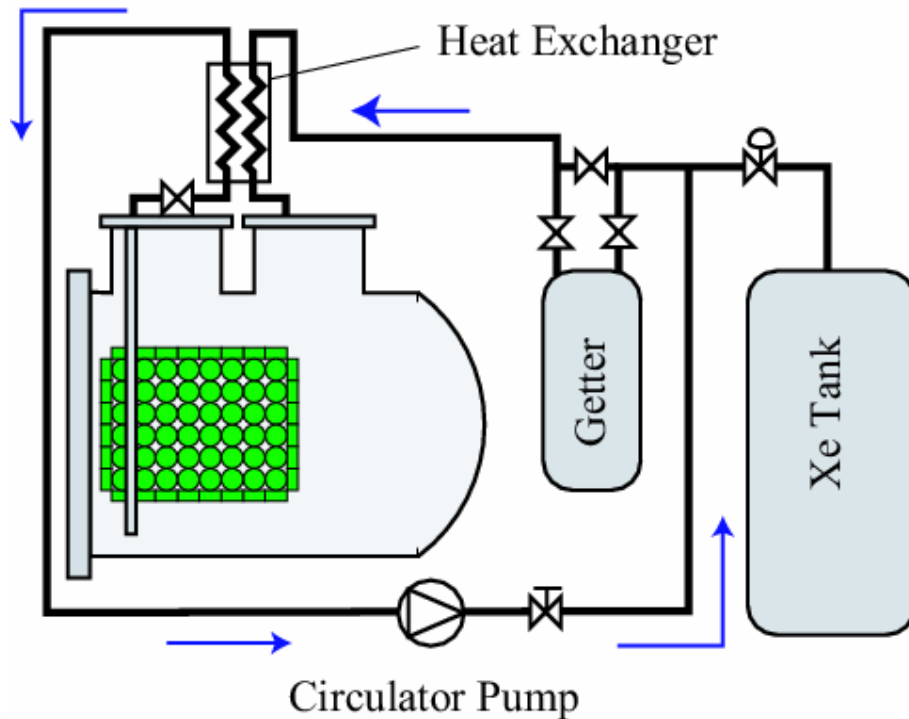


- Currently largest LXe detector in the world (224 PMTs, 150l),
- Contains all critical parts of final detector
- Can measure light attenuation length up to ~1m

Light attenuation

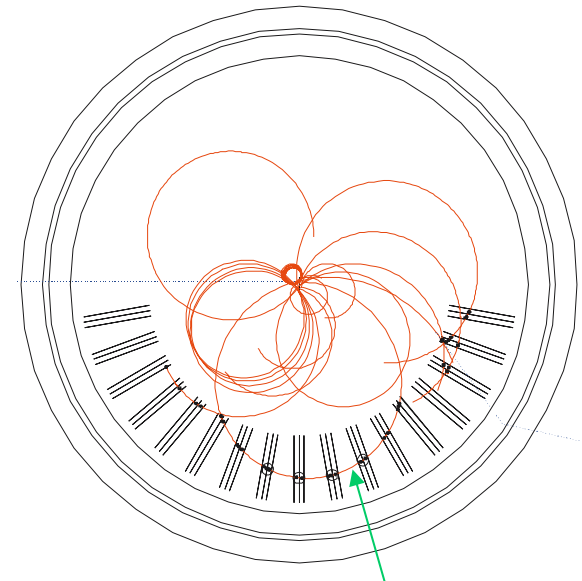
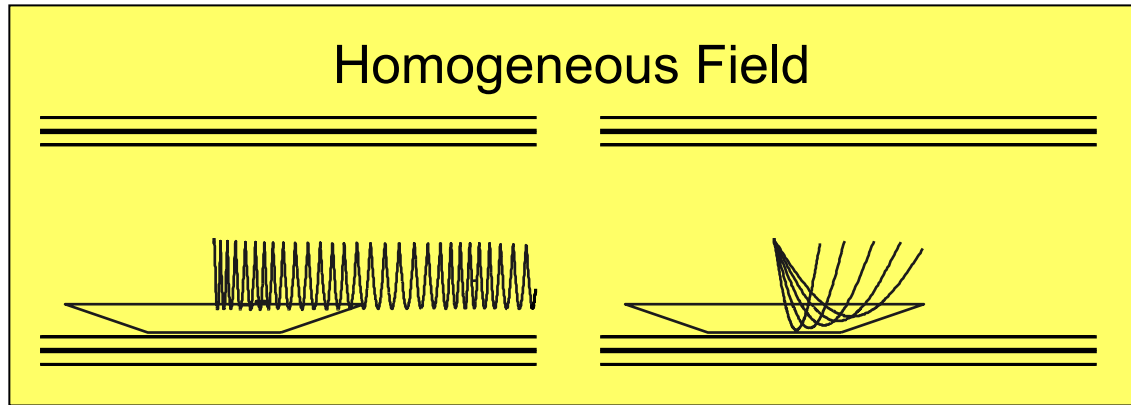
- Two beam tests, many α -source and cosmic runs in Tsukuba, Japan
- Light attenuation much too high ($\sim 10x$)
- Cause: ~ 3 ppm of Water in LXe
- Cleaning with "hot" Xe-gas before filling did not help
- Water from surfaces is only absorbed in LXe
- Constant purification necessary
- Filter system works, attenuation length can be improved with a time constant of ~ 350 hours

Purification System

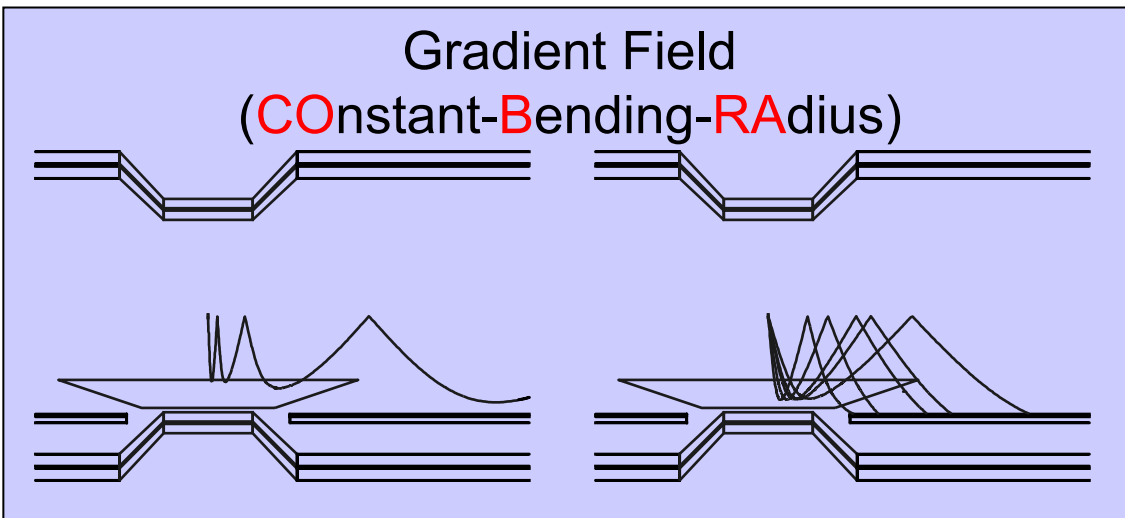


- Works acceptable in gas mode, $\tau = 350\text{h}$
- Should be much faster in liquid mode
- Redesign in progress

Positron Spectrometer



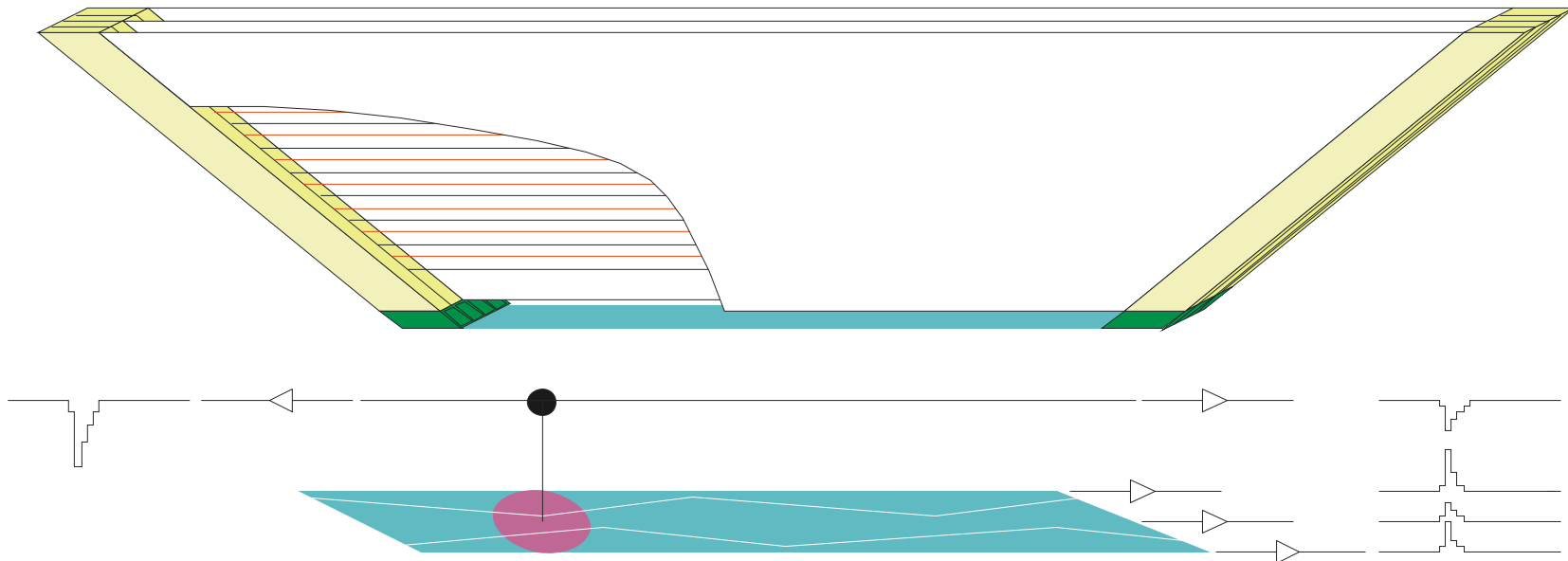
e^+ from $\mu^+ \rightarrow e^+ \gamma$



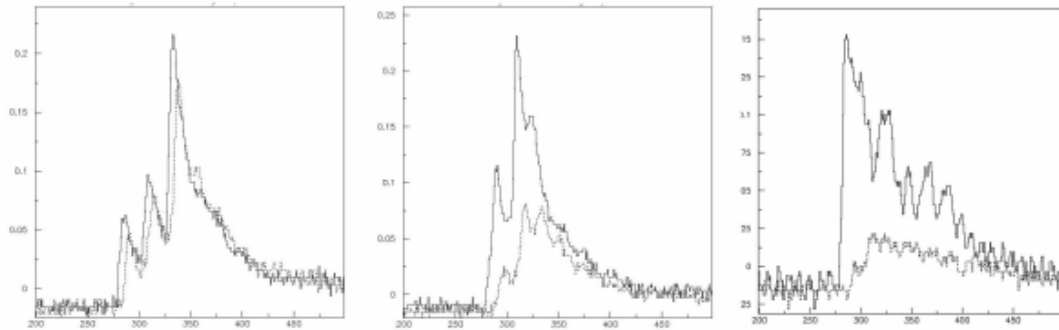
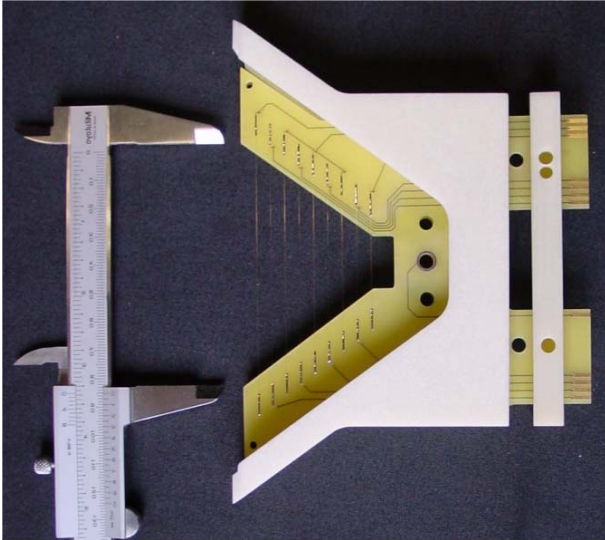
Ultra-Thin ($\sim 3\text{g/cm}^2$)
superconducting
solenoid
with 1.2 T field

Drift Chamber

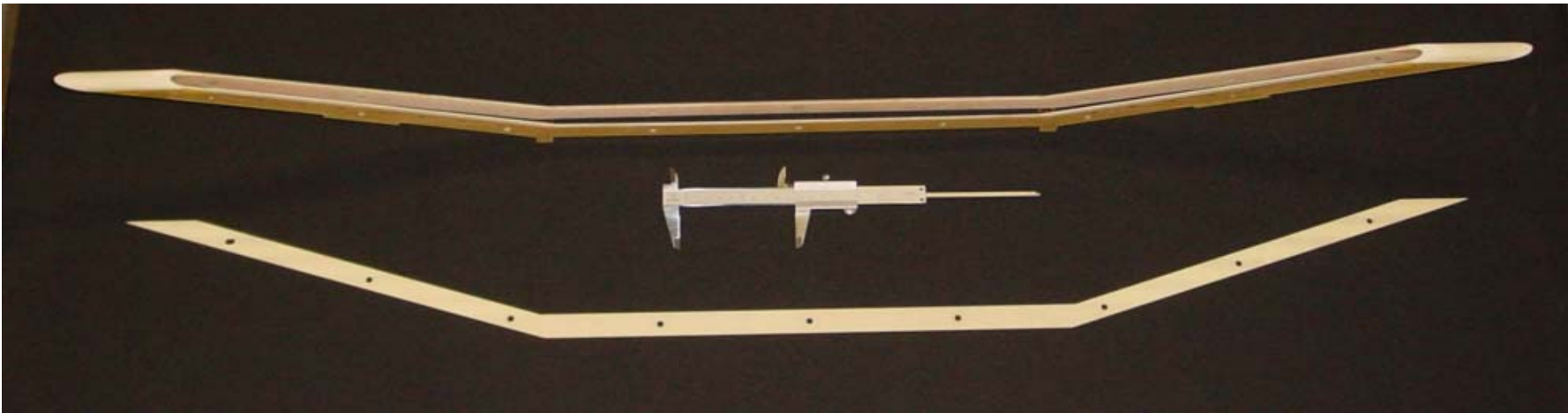
- 16 radial chambers with 20 wires each
- Staggered cells measure both position and time
- He - C₂H₆ gas to reduce multiple scattering
- Vernier pattern to determine z coordinate



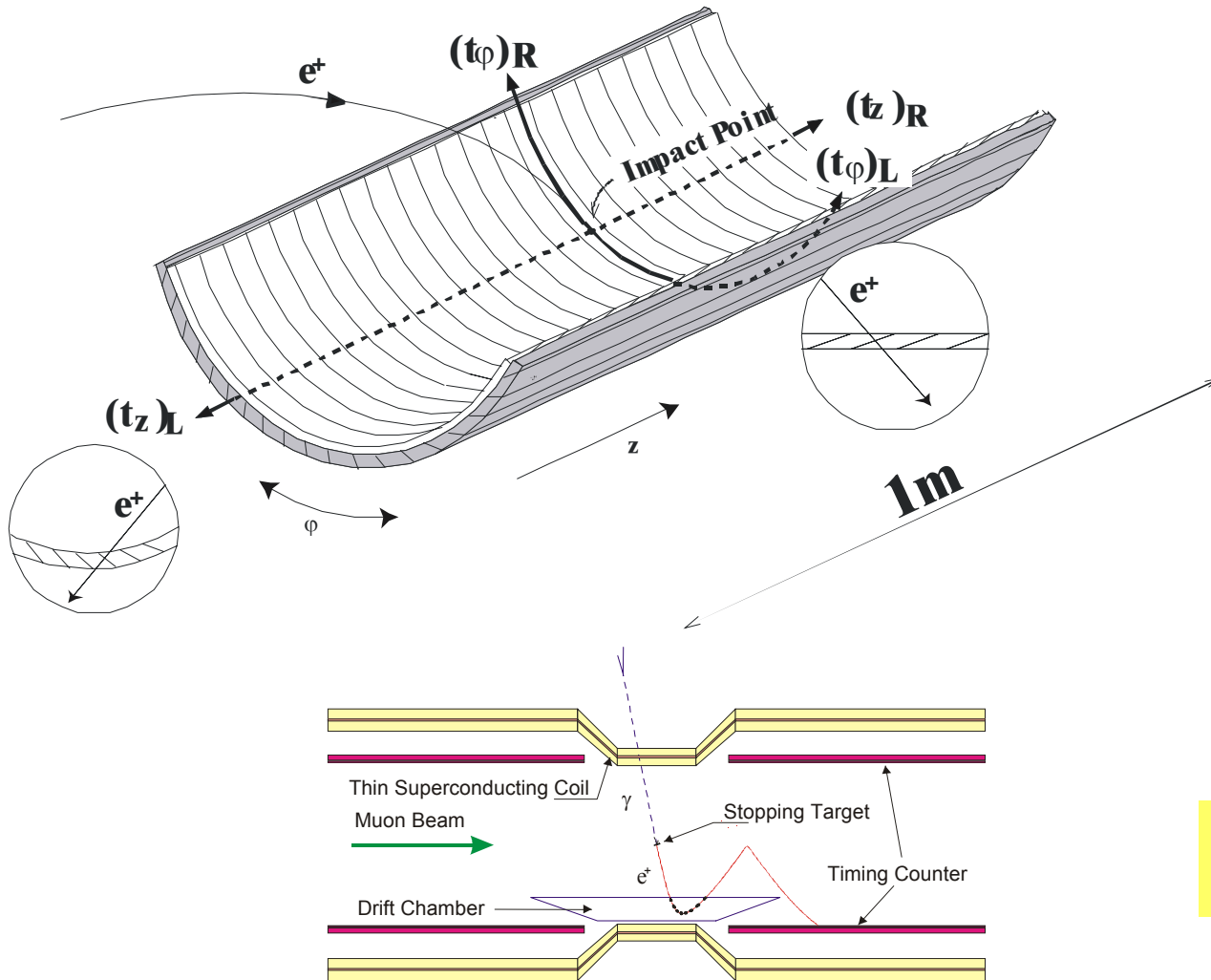
Prototype Tests at PSI



- Anode charge division tests successful
- Fast preamplifier with FADC (~200MHz)



Positron timing counter



- Aimed resolution $\sim 100\text{ps}$ FWHM
- Tests with cosmic μ confirmed 100ps FWHM

<http://meg.psi.ch/subprojects/tcount/>

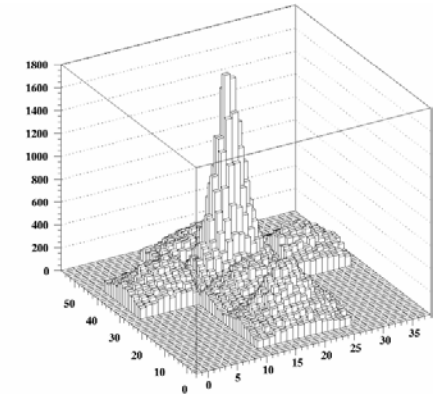
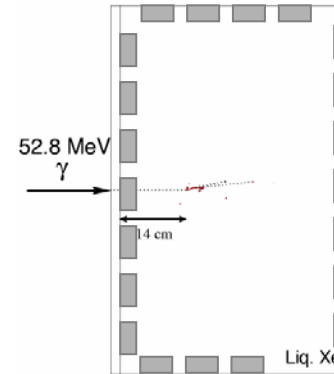
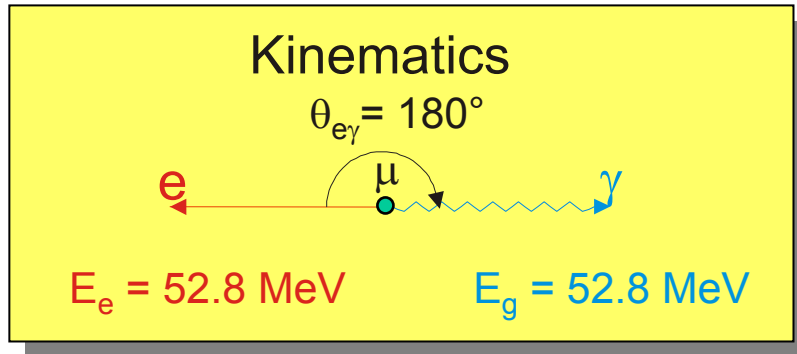
Status of R & D

- Complete MC, much improved compared to original proposal
- Beam line mode found with high rate and low contamination
- Positron detector prototype confirms 100ps resolution
- Large prototype LXe detector shows that originally planned resolution cannot be reached ($10^{-14} \rightarrow 10^{-13}$)
- Drift chamber seems to meet design goal
- We are confident that the experiment can be made at the 10^{-13} level with current technologies
- Start building full detector, develop electronics

Electronics

Trigger
DAQ
Slow-Control

Trigger Requirements



- ❖ Beam rate 10^8 s^{-1}
- ❖ Fast LXe energy sum $> 45 \text{ MeV}$ $2 \times 10^3 \text{ s}^{-1}$
- ❖ γ interaction point
- ❖ e^+ hit point in timing counter
- ❖ time correlation $\gamma - e^+$ 200 s^{-1}
- ❖ angular correlation $\gamma - e^+$ 20 s^{-1}

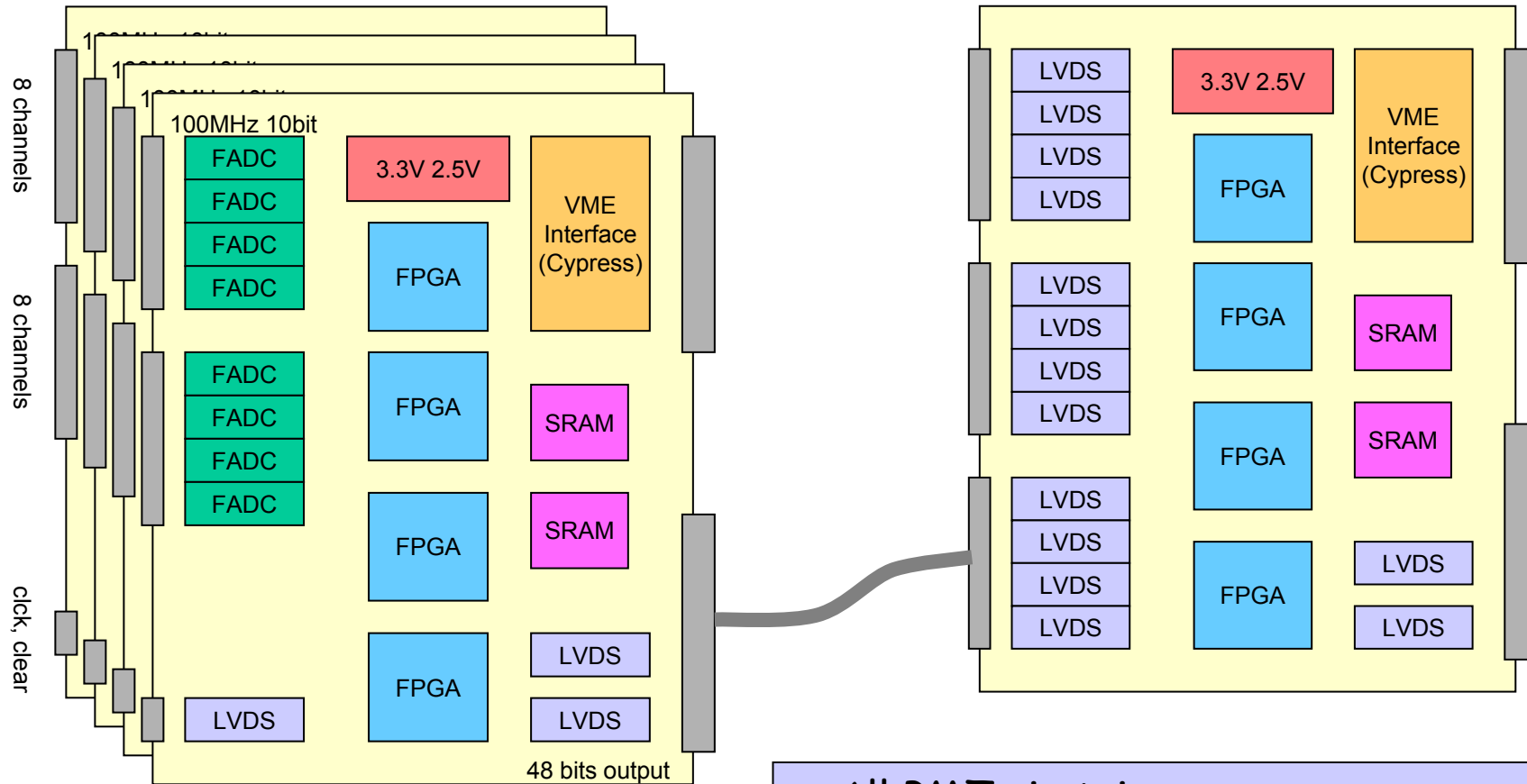
↑
M.C.

- Total ~ 800 PMTs
- Common noise contributes significantly to analog sum
- AC coupling \rightarrow Baseline drift
- How to evaluate θ, ϕ of shower center?

Digital Trigger

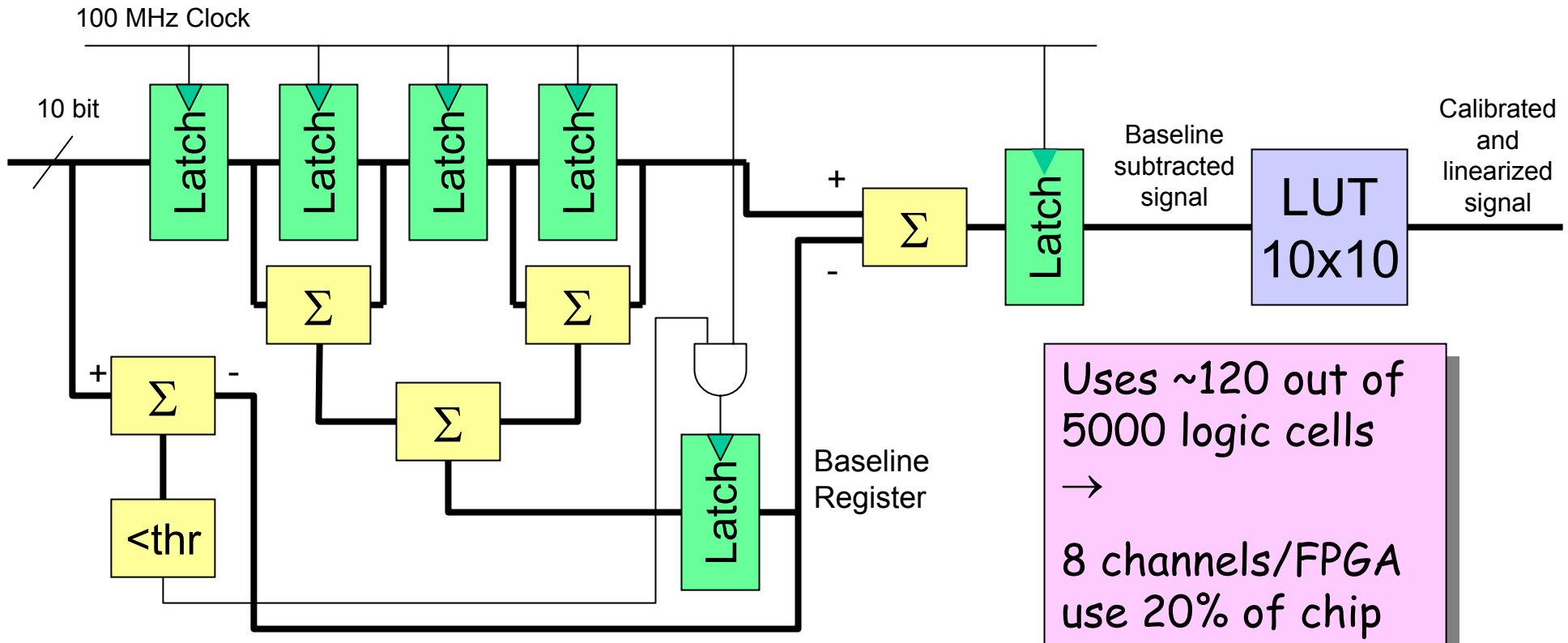
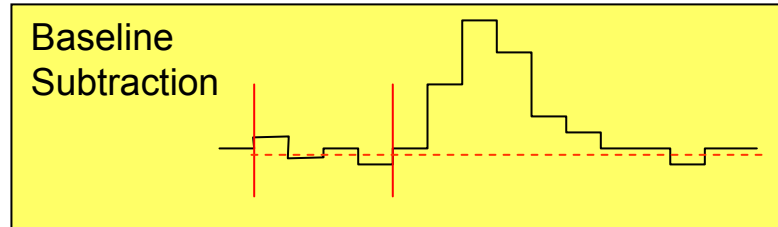
Type1

Type2



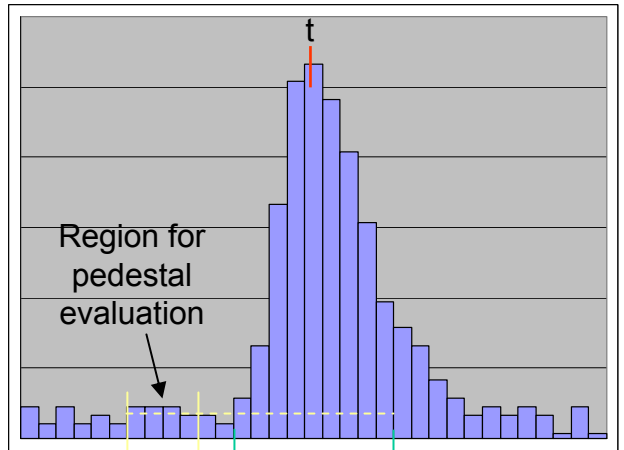
- All PMTs in trigger
- Board hierarchy with LVDS interconnect
- Use FPGA with double capacity

Baseline Subtraction

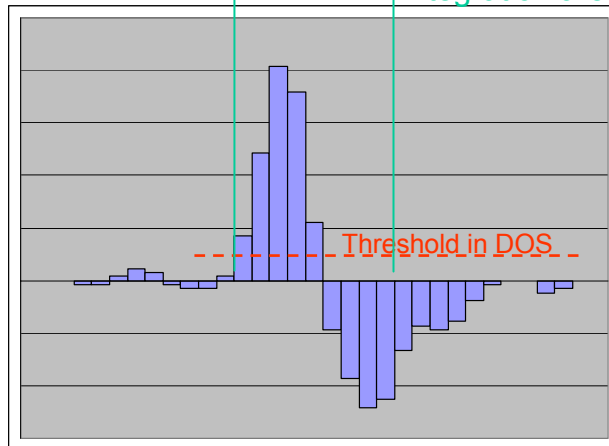


QT Algorithm

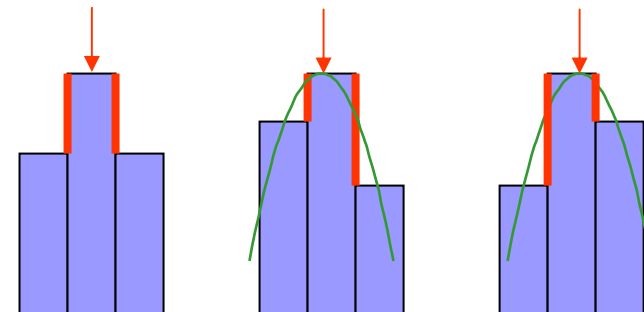
original waveform



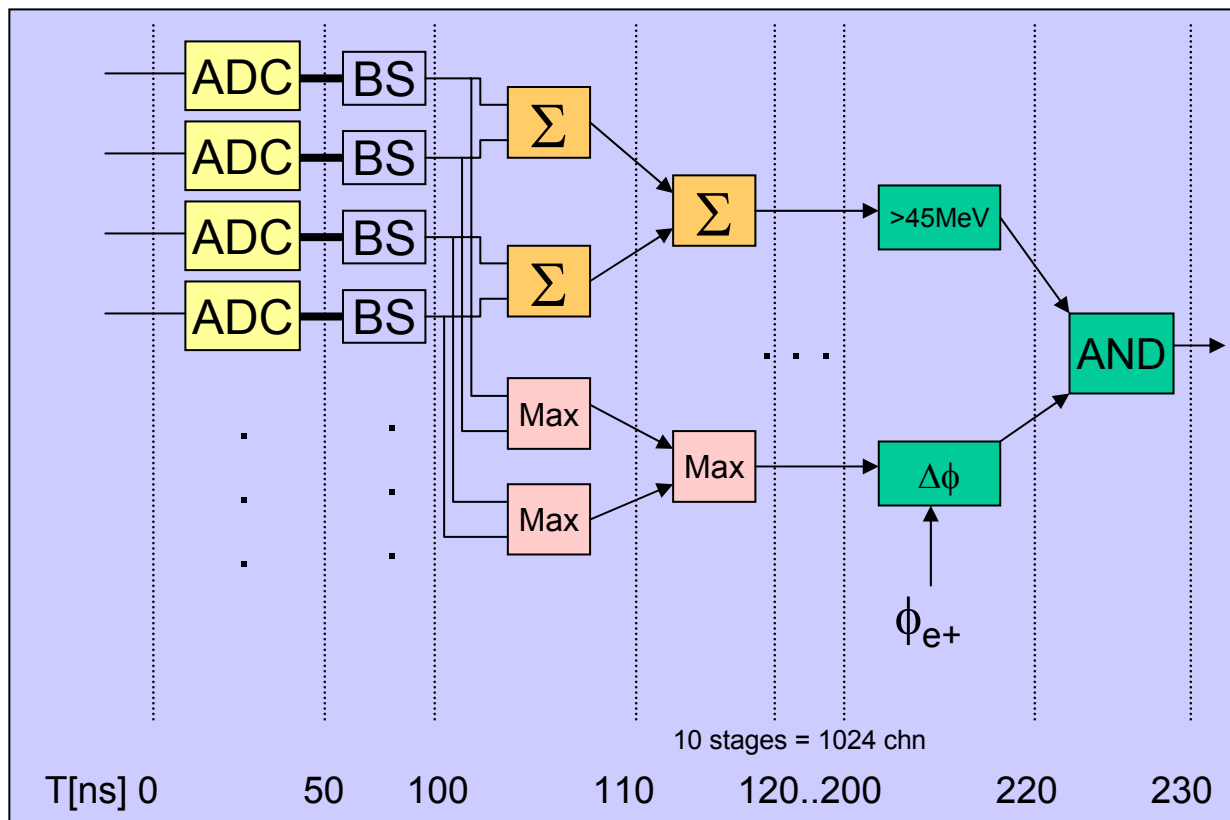
smoothed and differentiated
(Difference Of Samples)



- Inspired by H1 Fast Track Trigger (A. Schöning)
- Hit region defined when Difference of Samples is above threshold
- Integration of original signal in hit region
- Pedestal evaluated in region before hit
- Time interpolated using maximum value and two neighbor values in LUT → 1ns resolution for 10ns sampling time



Trigger latency

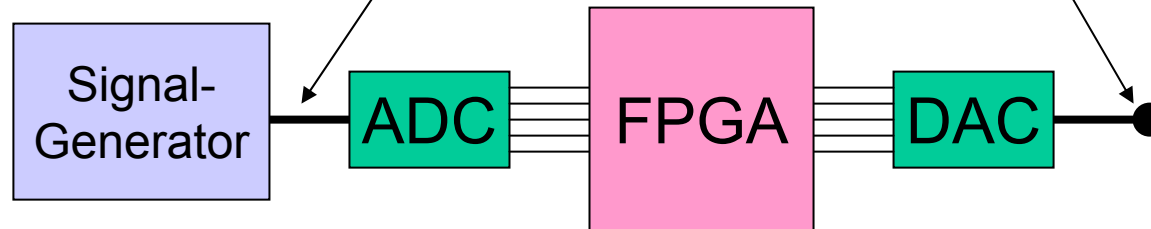
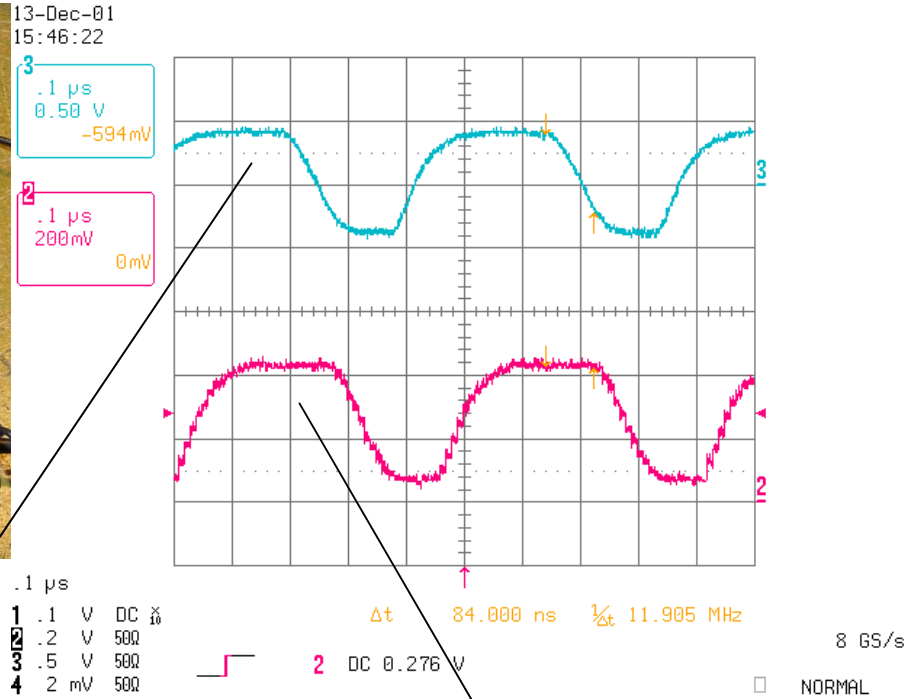
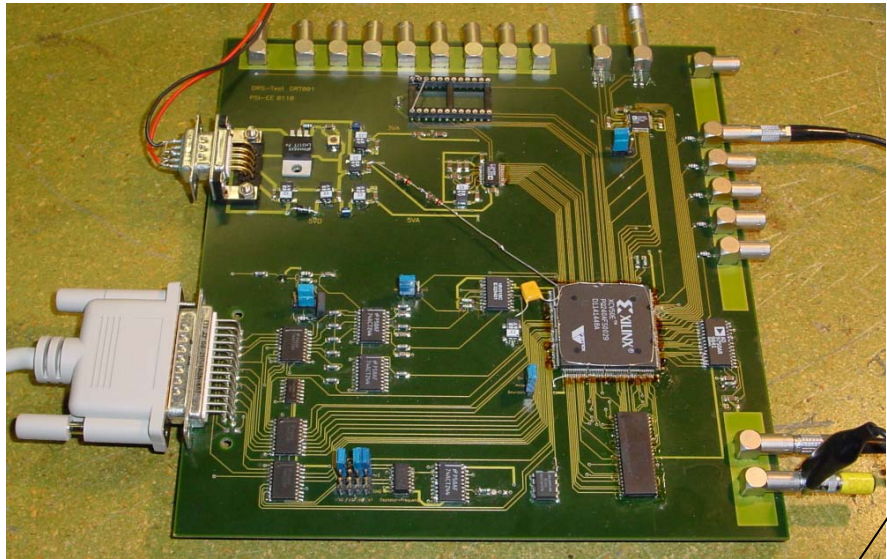


~600 chn. / 10 bit
At 100 MHz

→ 75 GB/s
processing power
In 2 VME crates

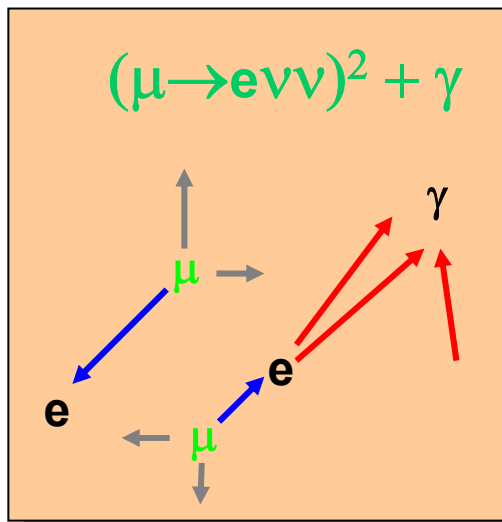
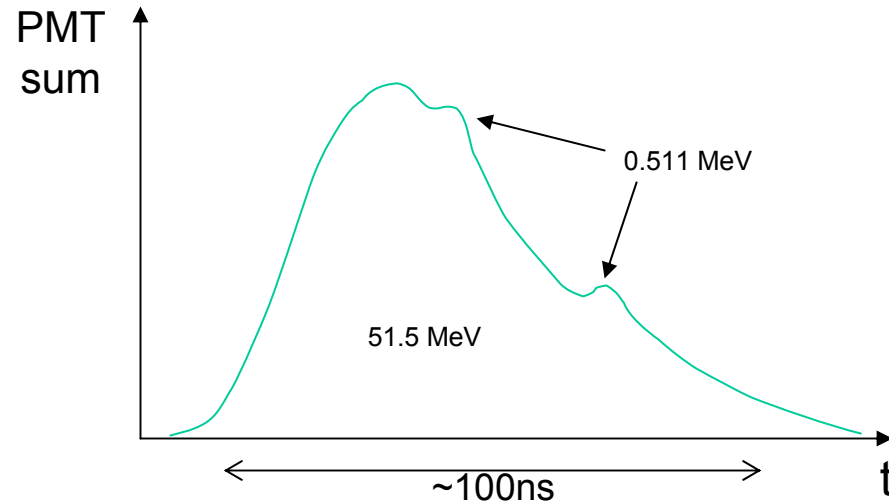
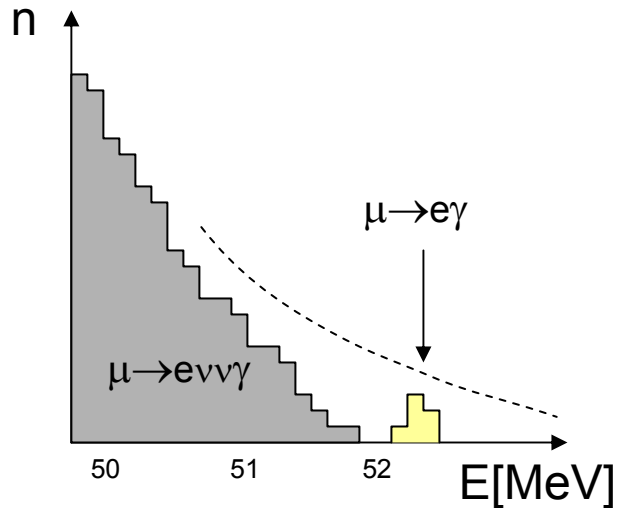
Inter-board communication: 120ns
Total: 350ns (simulated)

Prototype board



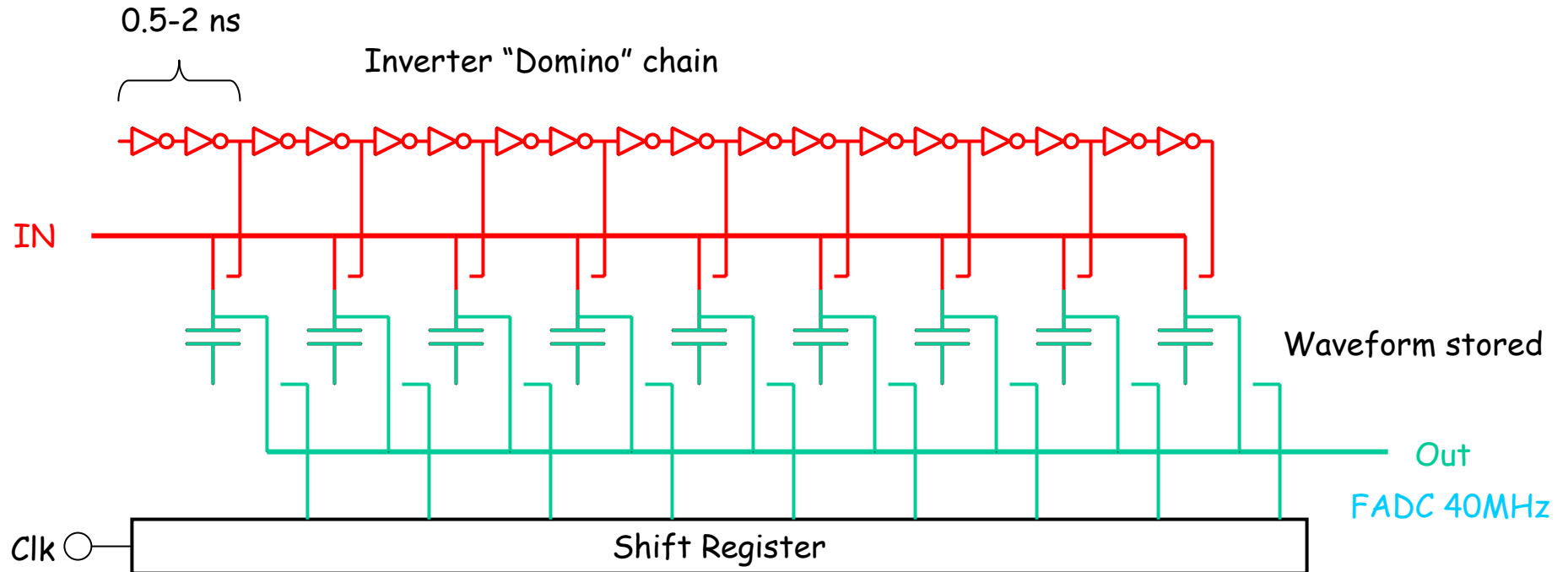
DAQ Electronics

DAQ Requirements



- γ 's hitting different parts of LXe can be separated if > 2 PMTs apart (15 cm)
- Timely separated γ 's need waveform digitizing $> 300 \text{ MHz}$
- If waveform digitizing gives timing $< 100\text{ps}$, no TDCs are needed

Domino Sampling Chip Principle



"Time stretcher" GHz → MHz

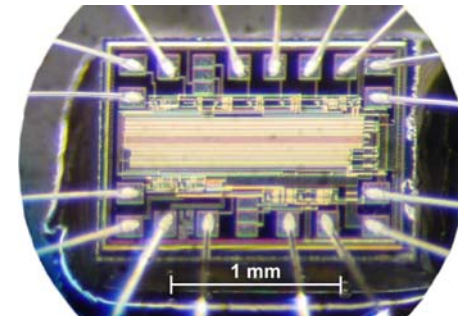
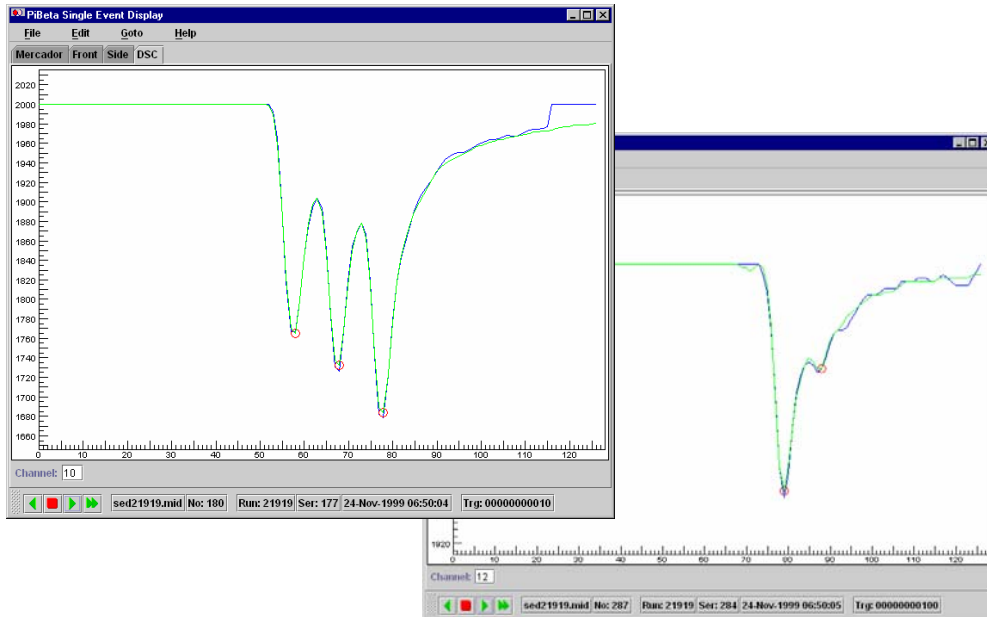
$\pi\beta$ Domino Sampling Chip

Existing:

- 0.5 - 1.2 GHz sampling speed
- 128 sampling cells
- Readout at 5 MHz, 12 bit
- ~ 60 \$/channel

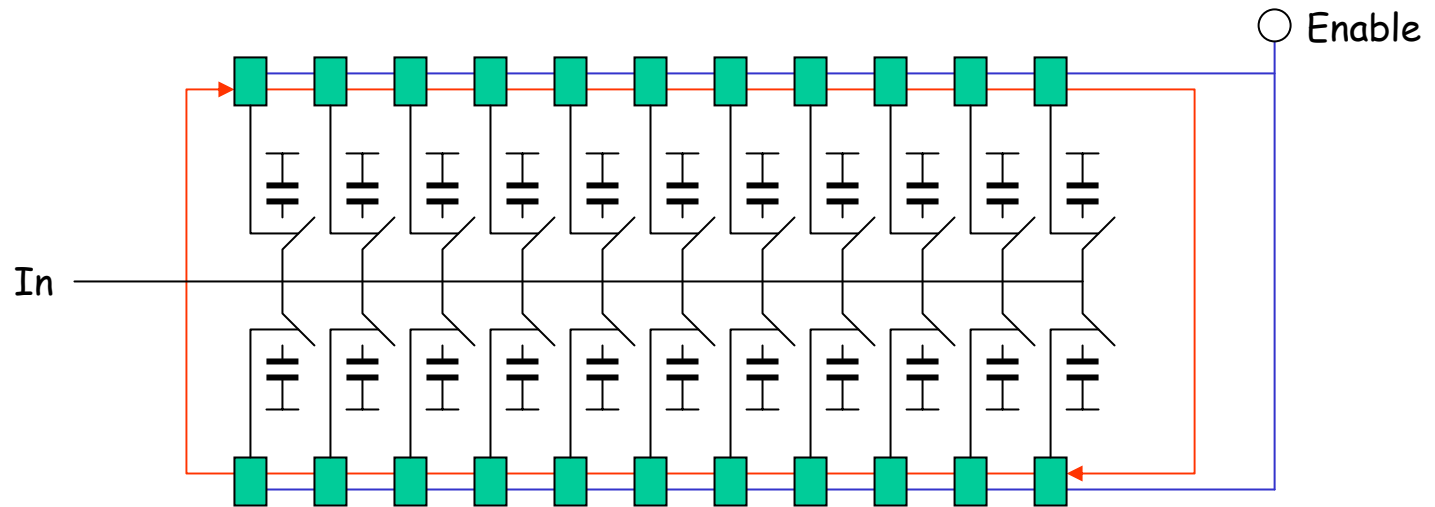
Needed:

- 2.5 GHz sampling speed
- Circular domino wave
- 1024 sampling cells
- 40 MHz readout
- < 100ps accuracy



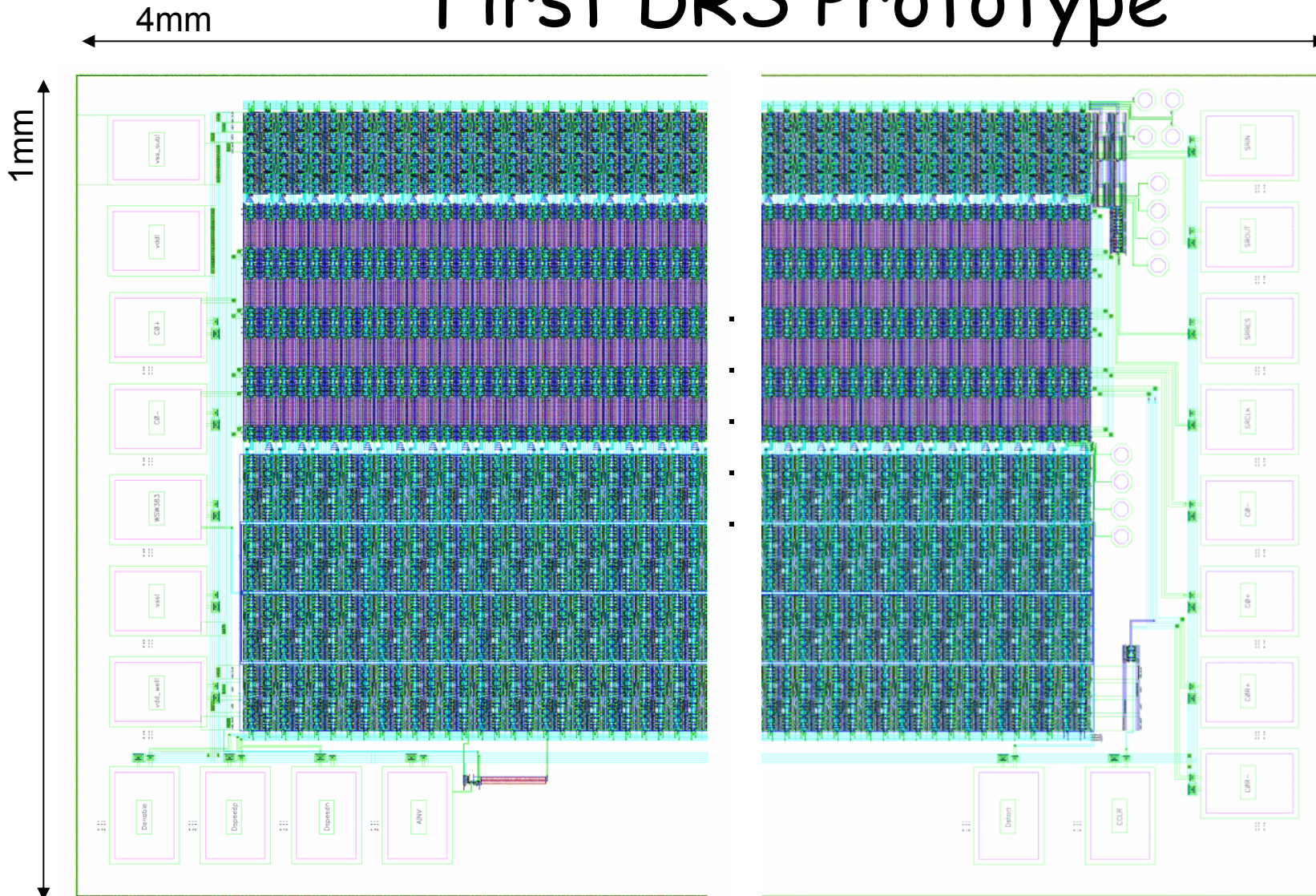
C. Brönnimann *et al.*, NIM A420 (1999) 264

Domino Ring Sampler (DRS)



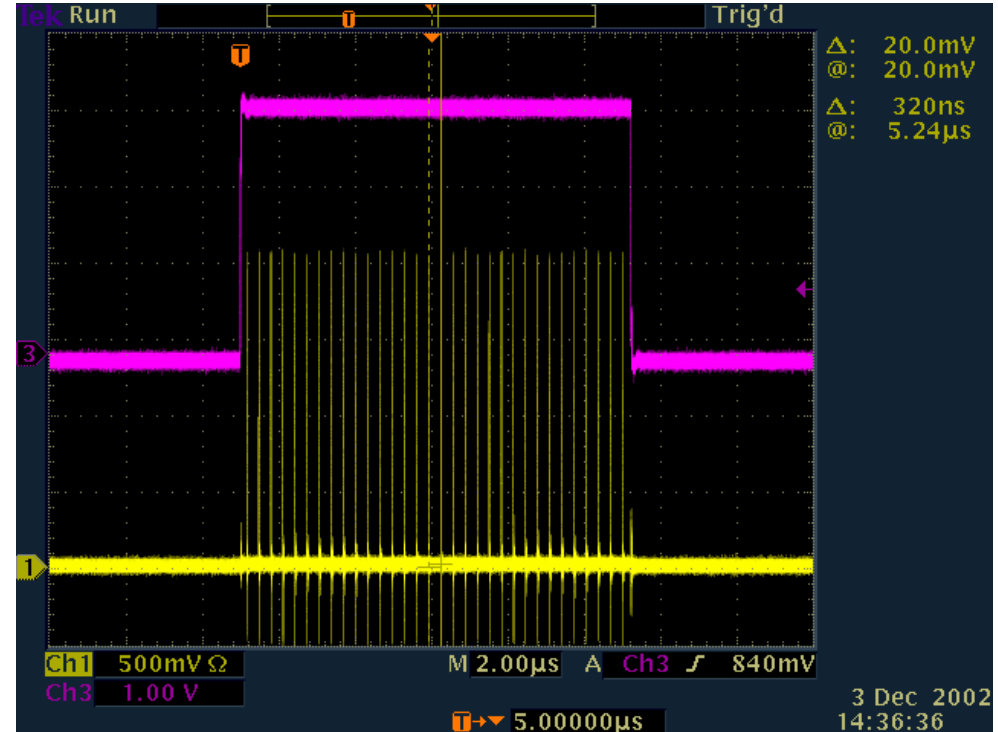
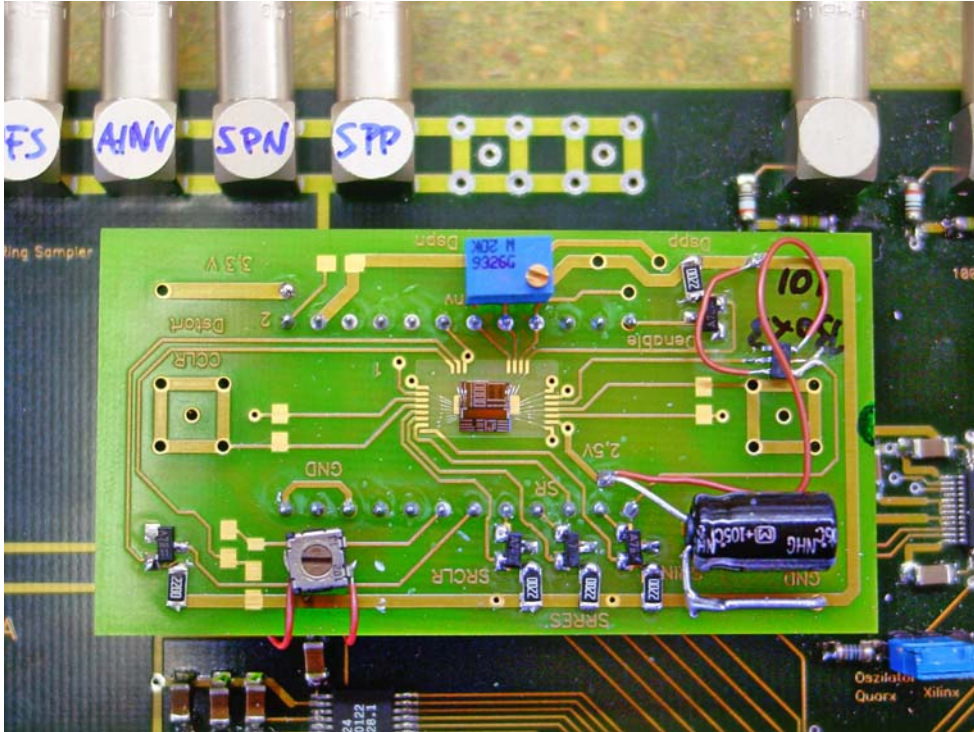
- Free running domino wave, stopped with trigger
- Sampling speed 2 GHz (500ps/bin), trigger gate sampling gives 50ps timing resolution
- 1024 bins \rightarrow 150ns waveform + 350ns delay

First DRS Prototype



- 0.25 μ m Process
- Radiation Hard
- Single Channel
- 768 cells

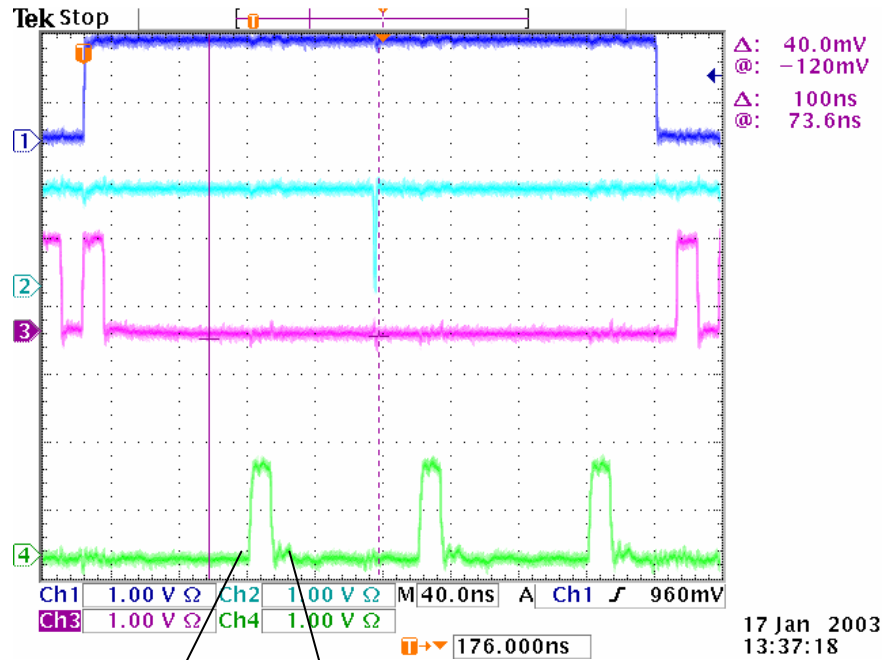
DRS Tests



- Sampling Speed 0.7 - 2.5 GHz
- Power Supply 35mW (@2.5V), 6mW (@1.8V)
- Timing jitter: 100ps

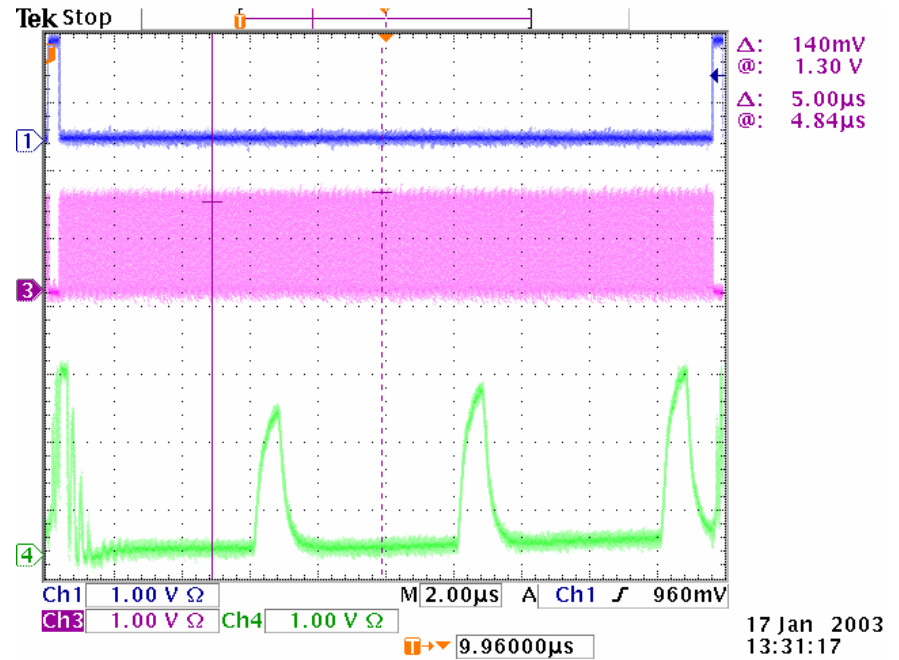
- Readout stable at 40 MHz
- TC: 0.2% / deg. C

Test Pulse Readout



12ns
Input pulses

17 Jan 2003
13:37:18

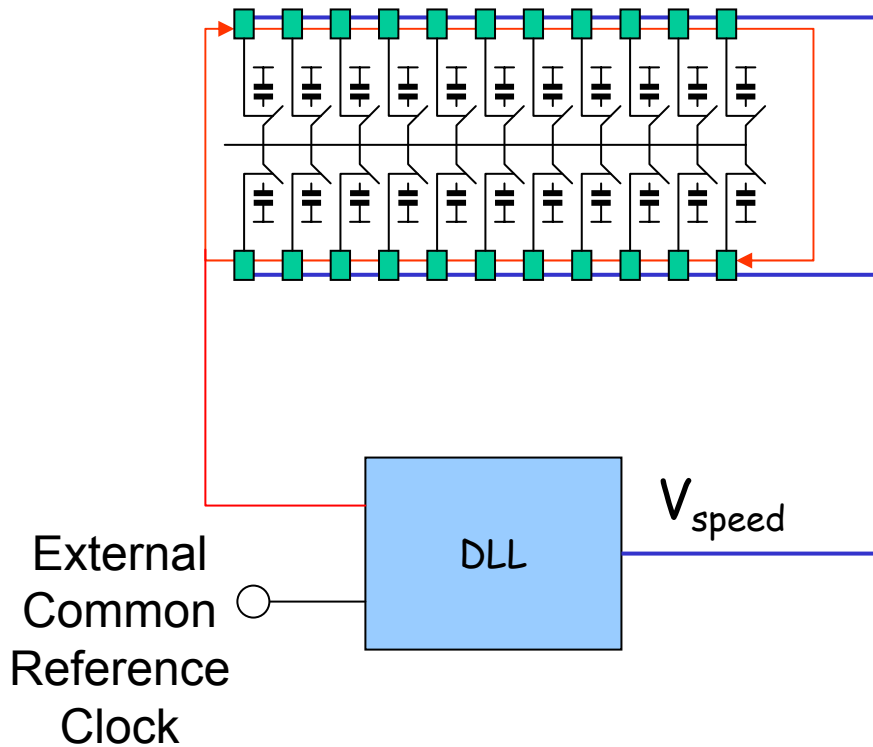


Limited by readout cirquetry!

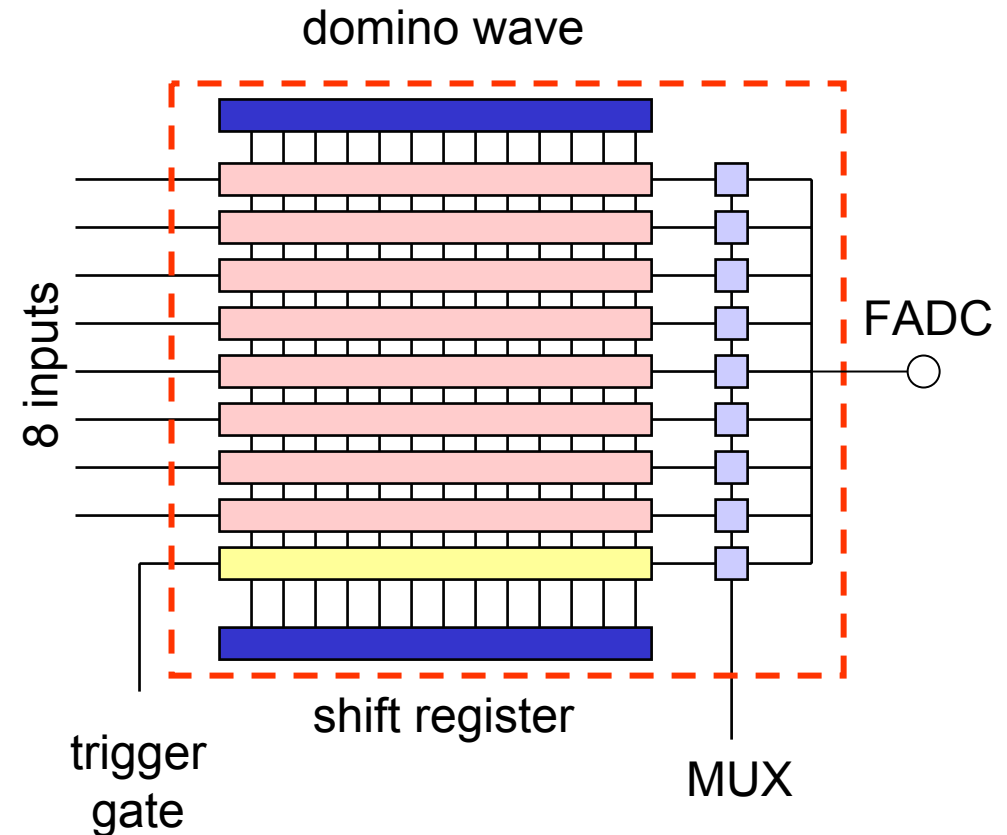
17 Jan 2003
13:31:17

Domino Wave Stabilization

Phase and Frequency Stabilization

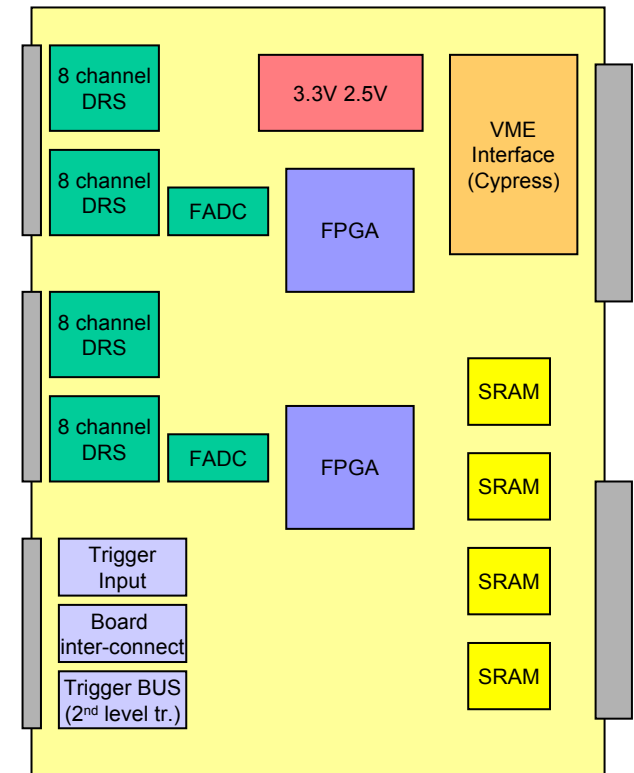
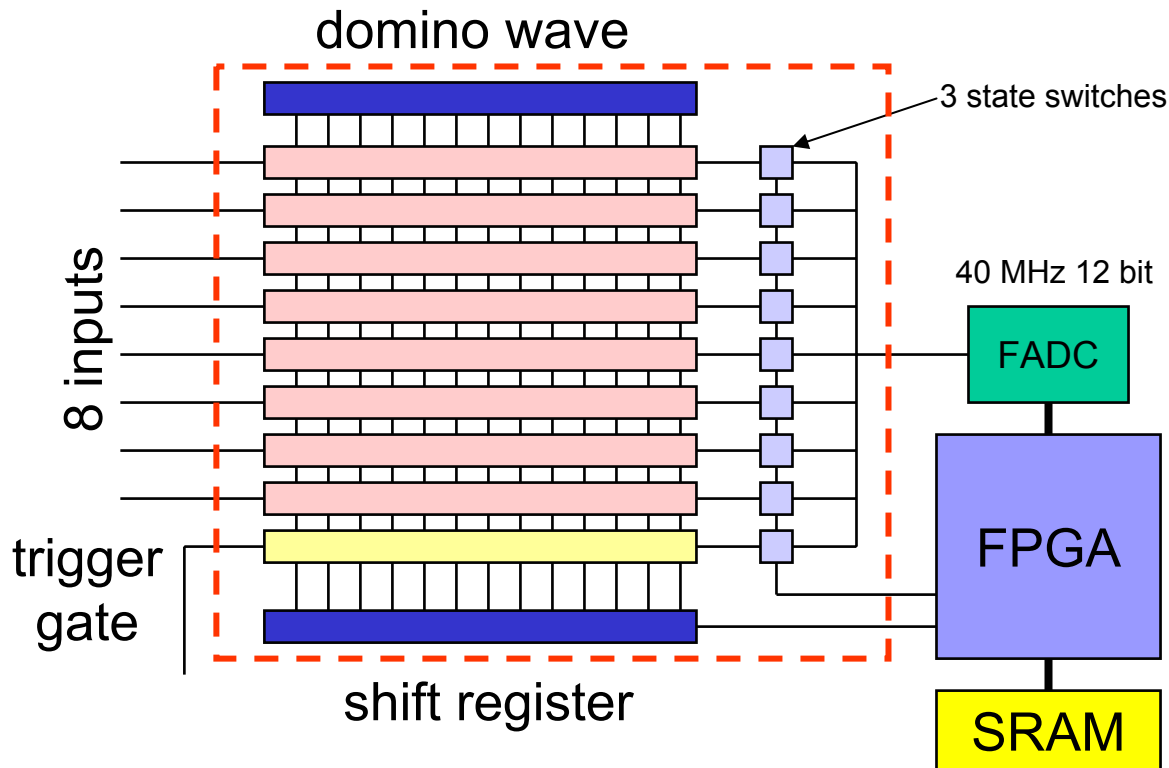


Trigger Signal Sampling

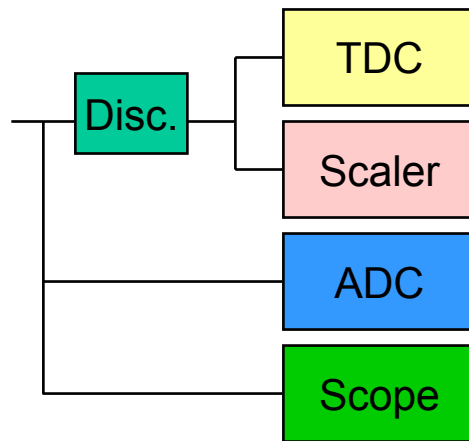


DAQ Board

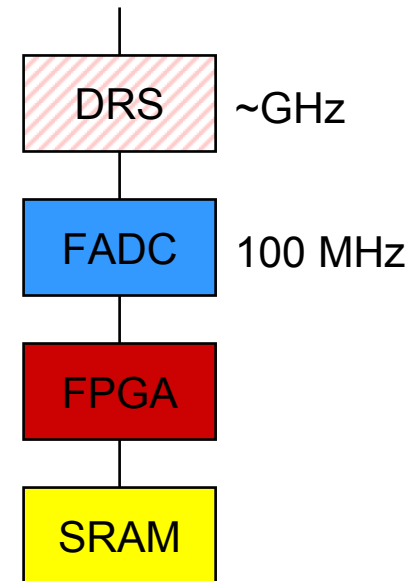
- 9 channels \times 1024 bins / 40 MHz = 230 μ s \rightarrow acceptable dead time
- Zero suppression in FPGA
- QT Algorithm in FPGA (store waveform if multi-hit)
- Costs per channel: ~25\$ (board) + 6\$ (chip)



"Redefinition" of DAQ

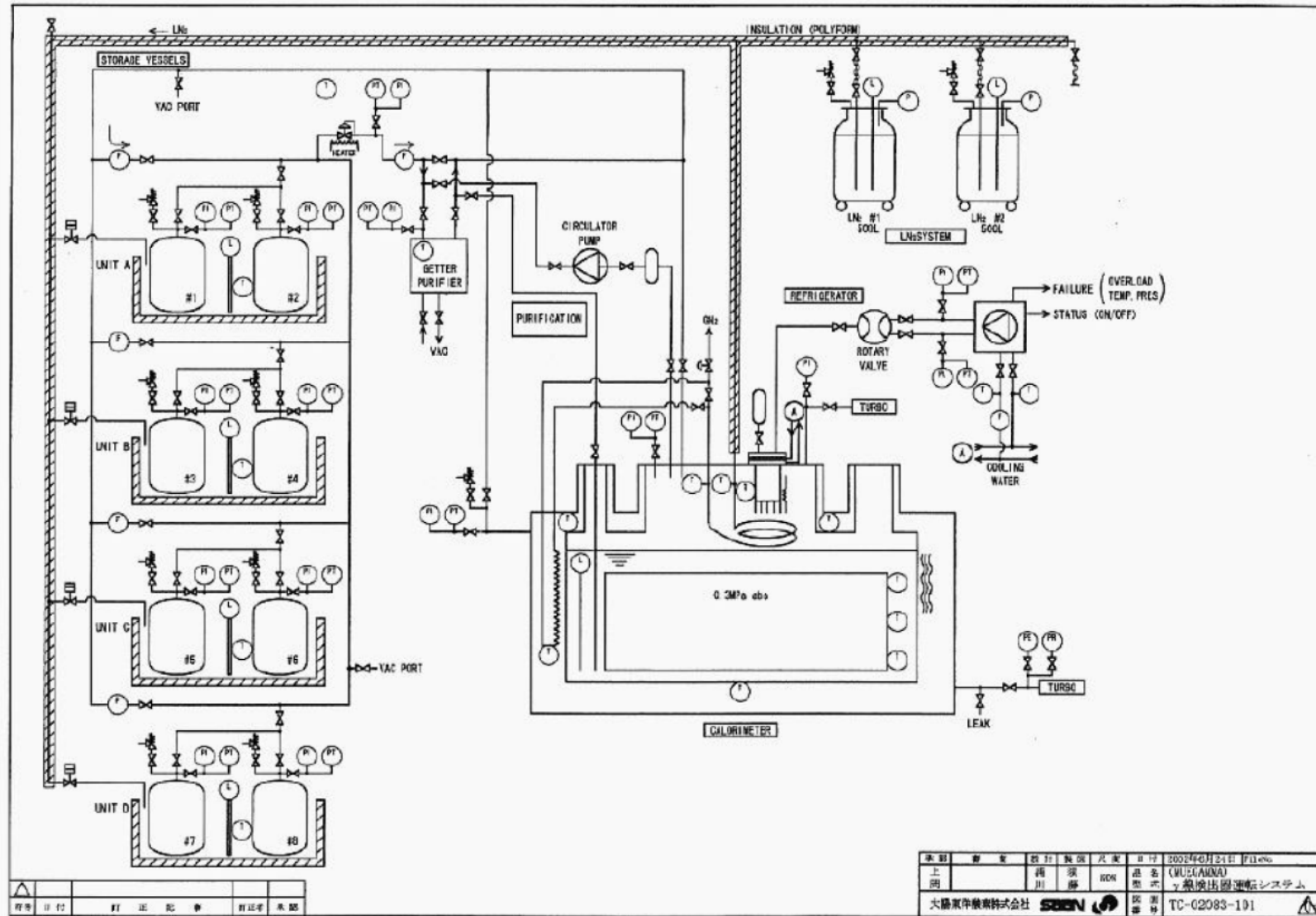


Conventional	New
AC coupling	Baseline subtraction
Const. Fract. Discriminator	DOS – Zero crossing
ADC	Numerical Integration
TDC	Bin interpolation (LUT) Waveform Fitting
Scaler (250 MHz)	Scaler (50 MHz)
Oscilloscope	Waveform sampling
400 US\$ / channel	50 US\$ / channel

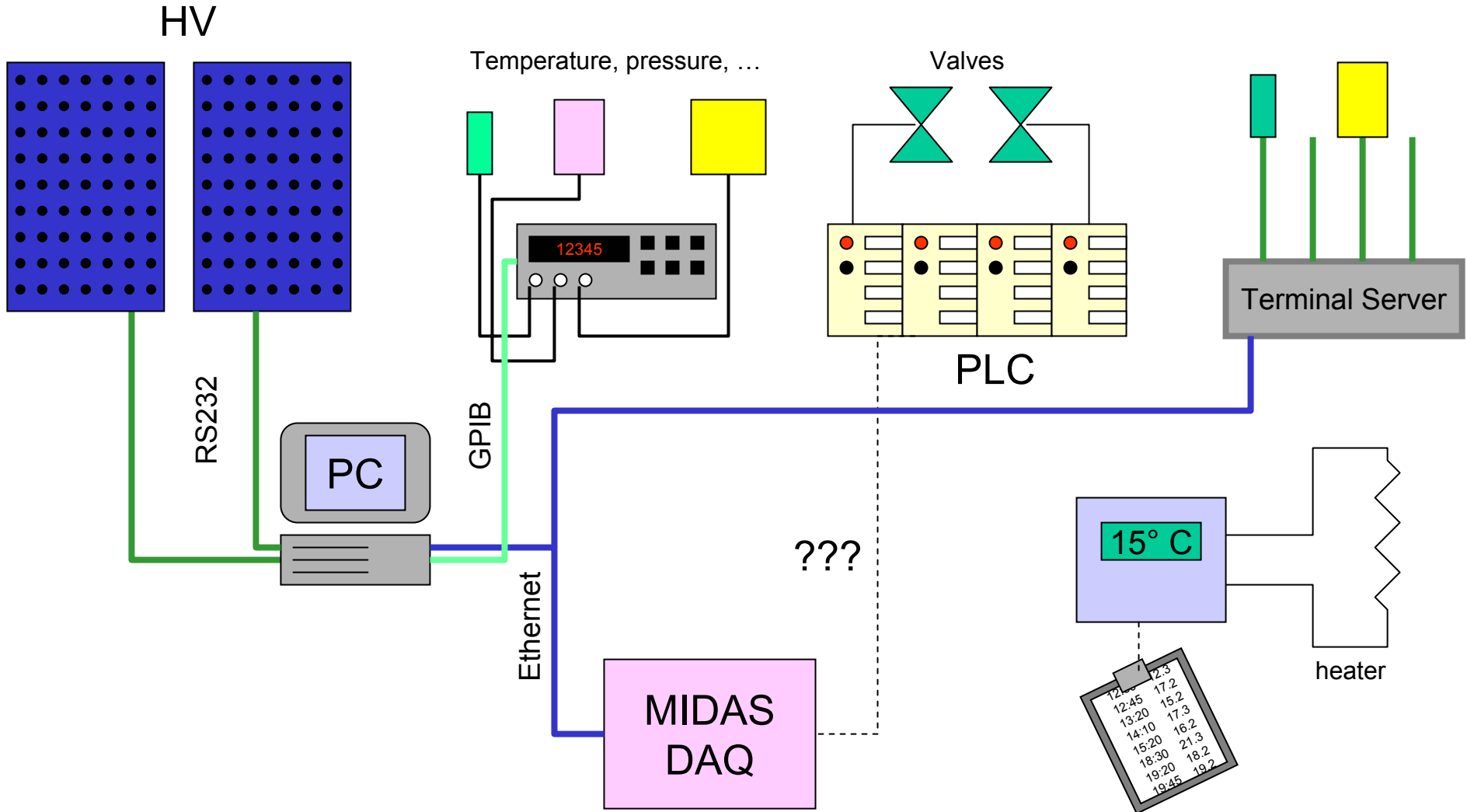


Slow Control

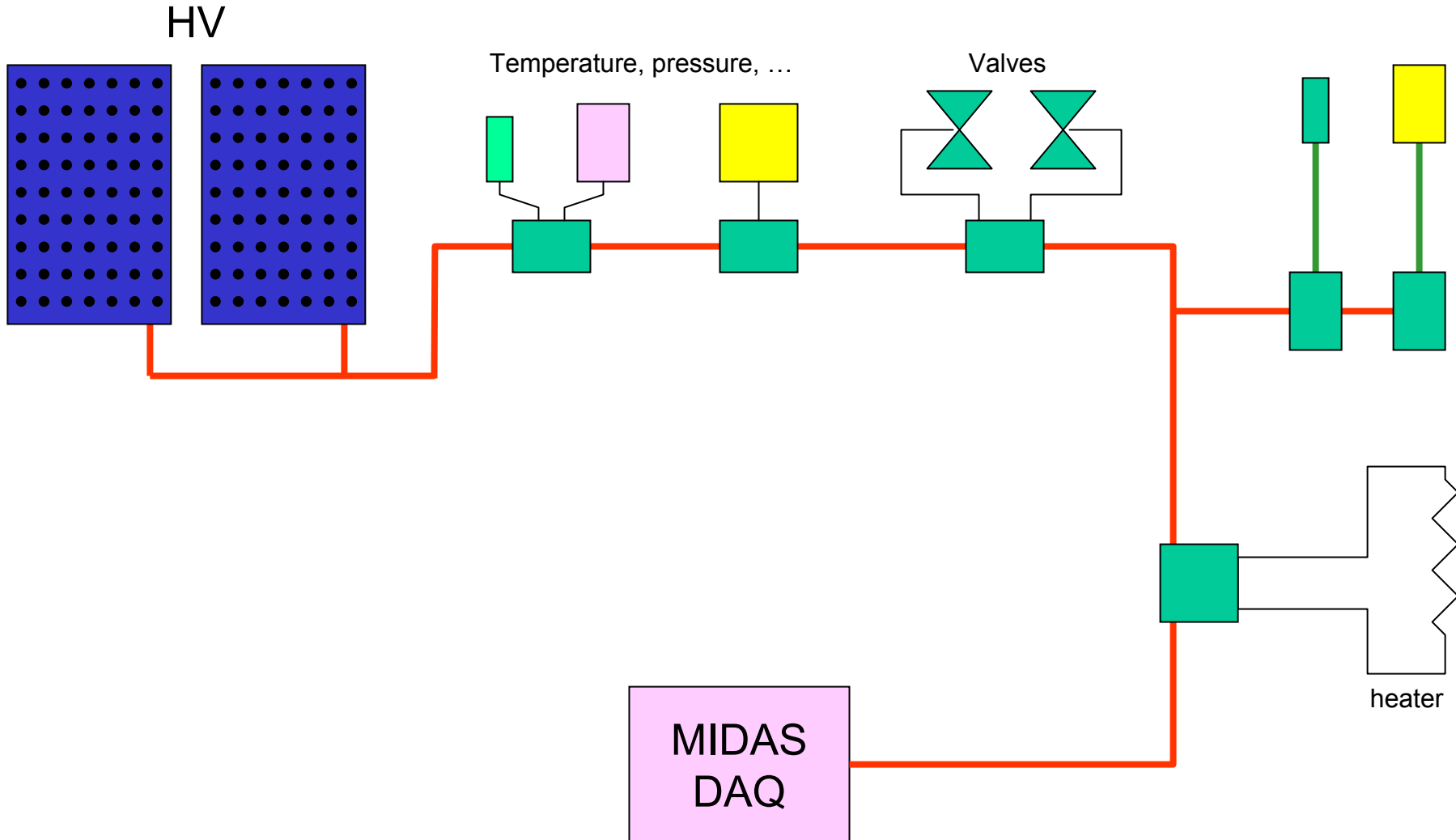
Cryogenics Design



Slow Control



Slow Control Bus



Field Bus Solutions

- CAN, Profibus, LON available
- Node with ADC >100\$
- Interoperability not guaranteed
- Protocol overhead
- Local CPU? User programmable?
- How to integrate in HV? (CAEN use CAENET)

ANALOG DEVICES MicroConverter™, Multichannel 12-Bit ADC with Embedded FLASH MCU
ADuC812

FEATURES
ANALOG I/O

APPLICATIONS
Intelligent Sensors (IEEE 1471.2 Compatible)
Portable PCs, Instruments, Systems

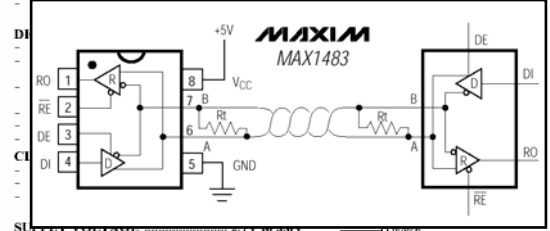
C8051F000/1/2/5/6/7
C8051F010/1/2/5/6/7

Mixed-Signal 32KB ISP FLASH MCU Family

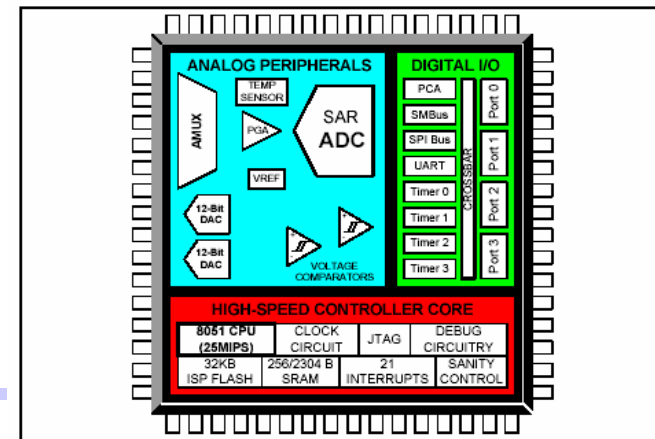
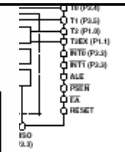


- ANALOG PERIPHERALS**
- SAR ADC
 - 12-bit (C8051F000/1/2, C8051F005/6/7)
 - 10-bit (C8051F010/1/2, C8051F015/6/7)
 - ±1LSB INL; No Missing Codes
 - Programmable Throughput up to 100kps
 - Up to 8 External Inputs; Programmable as Single-Ended or Differential
 - Programmable Amplifier Gain: 16, 8, 4, 2, 1, 0.5
 - Data Dependent Windowed Interrupt Generator
 - Built-in Temperature Sensor (±3°C)
 - Two 12-bit DACs
 - Two Analog Comparators
 - Programmable Hysteresis Values
 - Configurable to Generate Interrupts or Reset
 - Voltage Reference
 - 2.4V; 15 ppm/°C
 - Available on External Pin
 - Precision VDD Monitor/Brown-out Detector
- ON-CHIP JTAG DEBUG & BOUNDARY SCAN**
- On-Chip Debug Circuitry Facilitates Full Speed, Non-Intrusive In-System Debug (No Emulator Required!)
 - Provides Breakpoints, Single Stepping, Watchpoints, Stack Monitor
 - Inspect/Modify Memory and Registers
 - Superior Performance to Emulation Systems Using ICE-Chips, Target Pods, and Sockets
 - IEEE1149.1 Compliant Boundary Scan
 - Low Cost Development Kit

- HIGH SPEED 8051 µC CORE**
- Pipelined Instruction Architecture; Executes 70% of Instruction Set in 1 or 2 System Clocks
 - Up to 25MIPS Throughput with 25MHz Clock
 - 21 Vectored Interrupt Sources
- MEMORY**
- 256 Bytes Internal Data RAM (F000/01/02/10/11/12)
 - 2304 Bytes Internal Data RAM (F005/06/07/15/16/17)



64-Pin TQFP, 48-Pin TQFP, 32-Pin LQFP
Temperature Range: -40°C to +85°C

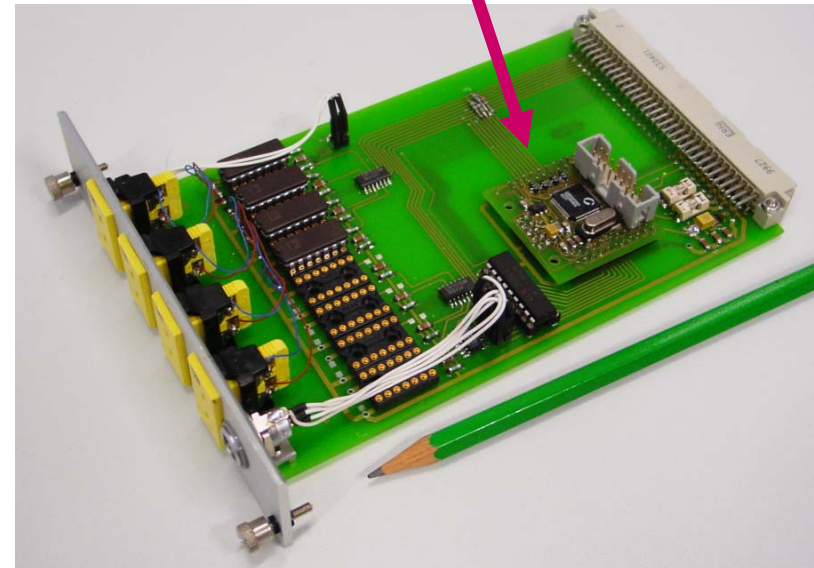
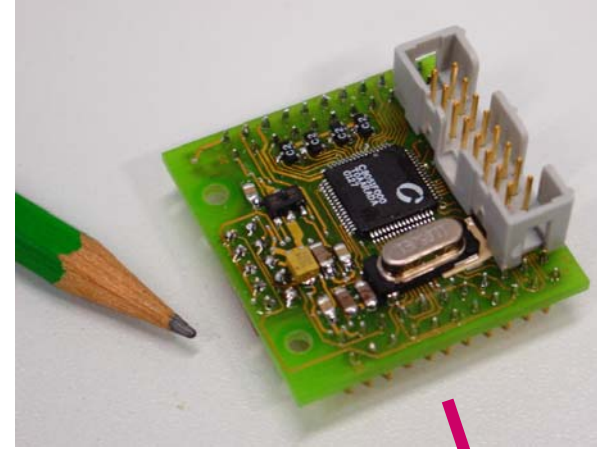


irwood, MA 02062-0106, U.S.A.
Site: <http://www.analog.com>
© Analog Devices, Inc., 1999

Generic node

- ADuC812 / C8051Fxxx Micro controllers with 8x12 bit ADC, 2x12 bit DAC, digital IO, 8051 μ C and Flash Memory
- RS485 bus over flat ribbon cable
- Powered through bus
- Costs ~30\$
- Piggy back board for signal conditioning cards

www.cygnal.com



2 Versions

BUS Oriented

- Generic node with signal conditioning
- RS232 node with protocol translator
- PC connection to parallel port (USB planned)
- Integration on sensors, in crates

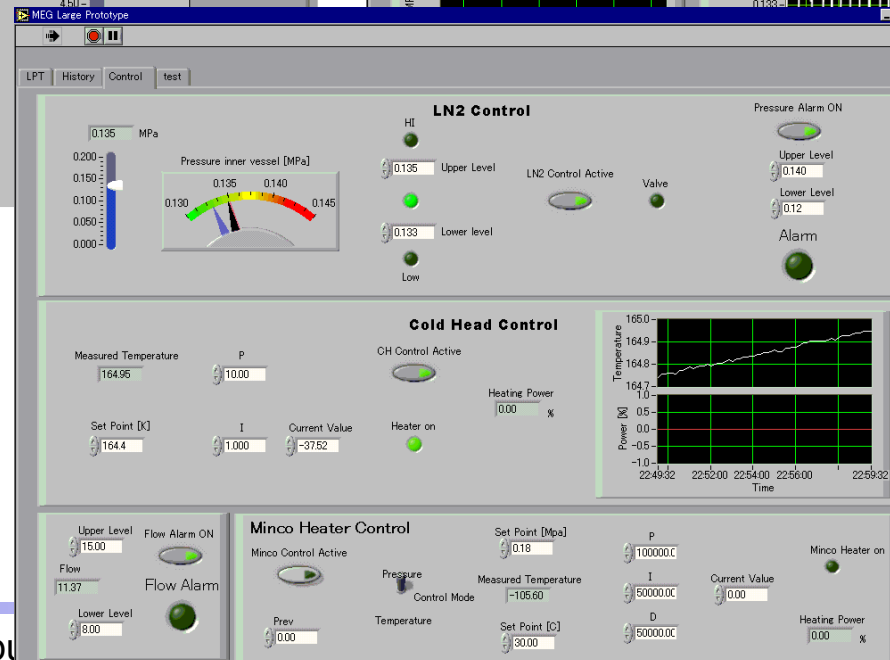
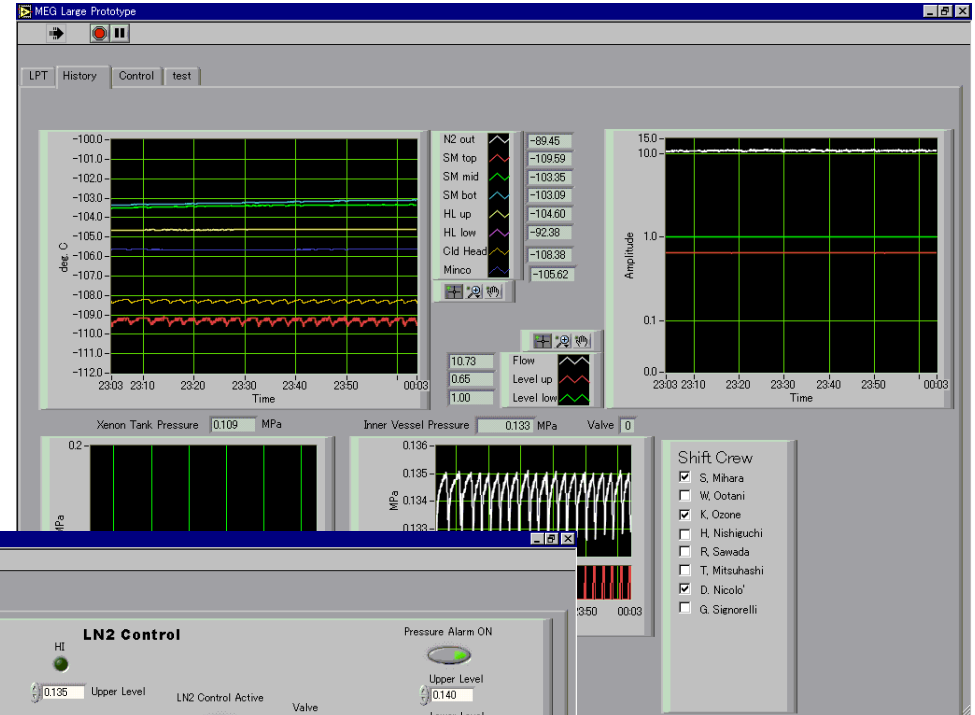
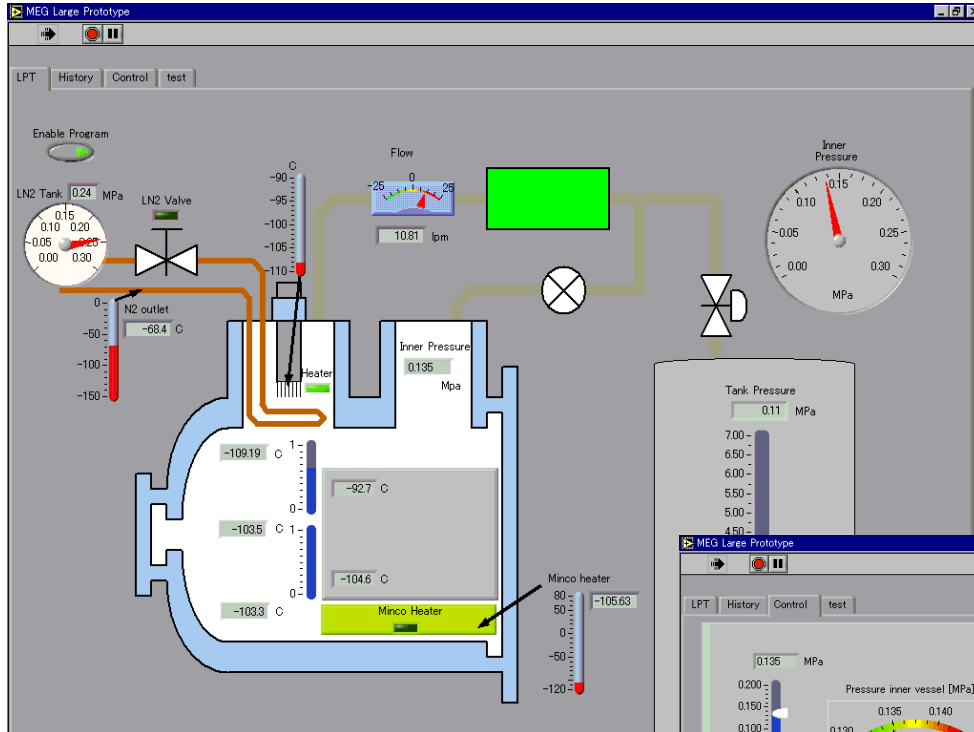
Can be mixed

Crate Oriented

- 19" crate with custom backplane
- Generic node as piggy-back
- Cards for analog IO / digital IO / °C / 220V
- Used in 3 experiments at PSI



Labview control of Large Prototype



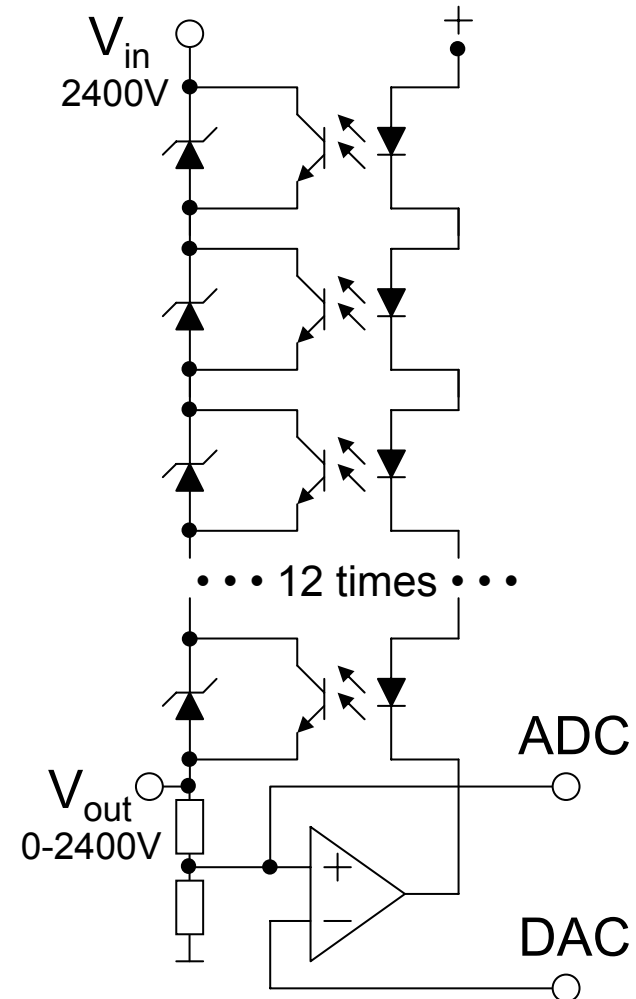
Midas Slow Control Bus

- 256 nodes, **65536 nodes** with one level of repeaters
- Bus length **~500m** opto-isolated
- Boards for voltage, current, temperature, Digital IO, 220V
- Readout speed: **0.3s for 1000 channels**
- C library, command-line utility, Midas driver, LabView driver
- Nodes are "self-documenting"
- Configuration parameters in EEPROM on node
- Node CPU can operate autonomously for interlock and regulation (PID) tasks (C programmable, floating point library)
- Nodes can be **reprogrammed** over network

<http://midas.psi.ch/mscb>

HV System Design

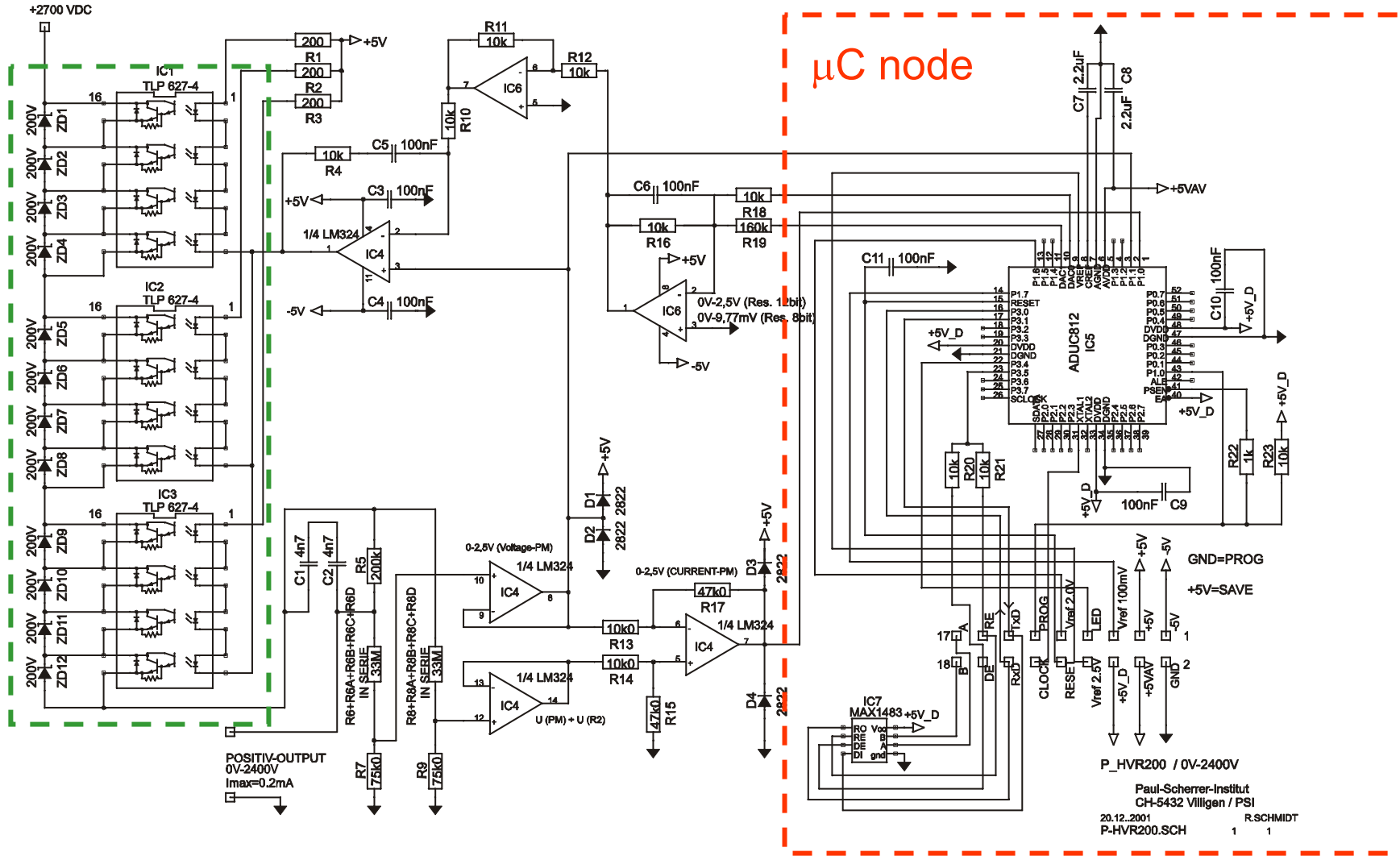
- Cheap and stable ($<0.3V$) HV system
- Regulate global external voltage
- Use series of opto-couplers
- Compensate non-linearities of opto-couplers by regulation loop
- ADC and DAC from slow control node



High Voltage System

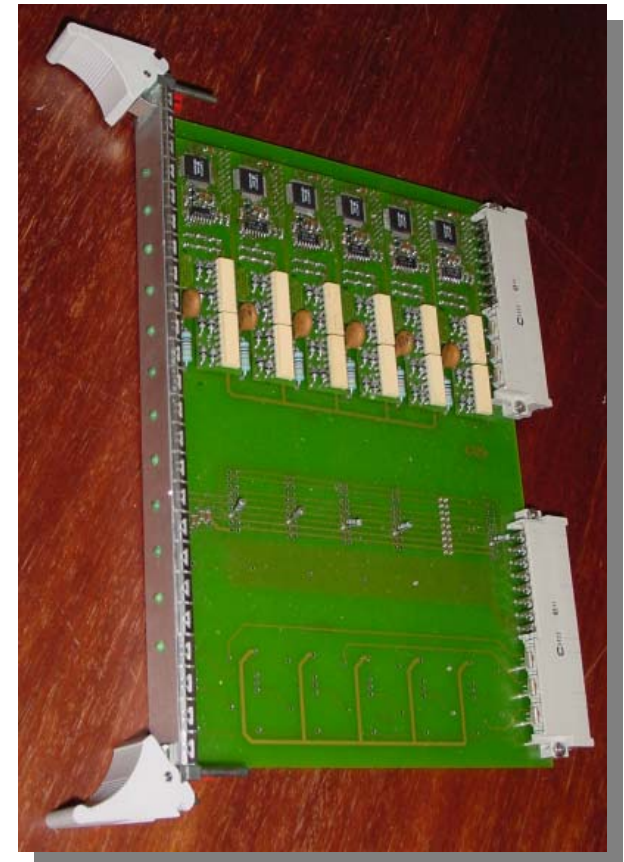
External HV

Opto-couplers



HV performance

- Regulates common HV source
- 0-2400V, ~1mA
- DAC 16bit, ADC 15bit
- Current trip $\sim 10\mu\text{s}$
- Self-calibration with two high accuracy reference voltages
- Accuracy $< 0.3\text{V}$ absolute
- Boards with 12 channels, crates with 192 channels
- **30\$/channel** (+ext. HV)



Prototype

Conclusions

Current Status

- R & D is going well, we are confident that we can do the experiment at the 10^{-13} level
- Assembly of main experiment has been started
- Physics runs expected in 2005
- Innovative electronics useful for other experiments
 - FPGA-based trigger with 100MHz FADC
 - 2 GHz cheap waveform sampling
 - New slow control system MSCB

<http://meg.psi.ch>